

SKFM1040CW-D2 THRU SKFM10200CW-D2**List**

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SKFM1040CW-D2 THRU SKFM10200CW-D2

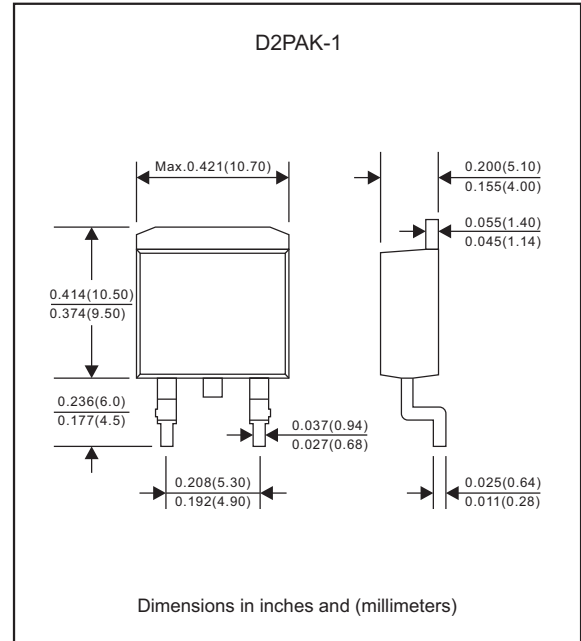
**10.0A Surface Mount
Schottky Barrier Rectifiers 40V-200V**

Features

- Batch process design, excellent power dissipation offers better reverse leakage current and thermal resistance.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- High surge capability.
- Guardring for overvoltage protection.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228
- Suffix "-H" indicates Halogen free parts, ex. SKFM1040CW-D2-H.

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, TO-263 / D2PAK-1
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any
- Weight : Approximated 1.46 gram

Package outline**Maximum Ratings** (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	SKFM 1040CW-D2	SKFM 1045CW-D2	SKFM 1060CW-D2	SKFM 10100CW-D2	SKFM 10150CW-D2	SKFM 10200CW-D2	UNIT
Repetitive peak reverse voltage	V _{RRM}	40	45	60	100	150	200	V
RMS voltage	V _{RMS}	28	31.5	42	70	105	140	V
DC blocking voltage	V _{DC}	40	45	60	100	150	200	V
Average forward rectified current per device per diode	I _O	10 5						A
Peak forward surge current 8.3ms single half sine-wave(JEDEC method)	I _{FSM}	120						A
Operating junction temperature range	T _J	-55 to +125		-55 to +150				°C
Storage temperature range	T _{STG}	-55 to +150						°C

Maximum Electrical Characteristics (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	SKFM 1040CW-D2	SKFM 1045CW-D2	SKFM 1060CW-D2	SKFM 10100CW-D2	SKFM 10150CW-D2	SKFM 10200CW-D2	UNIT
Forward voltage per leg at If=5A	V _F	0.55		0.70	0.85	0.95		V
DC reverse current at T _A =25°C at rated DC blocking voltage at T _A =125°C	I _R	0.5 50			0.05 10			mA

Thermal Characteristics

PARAMETER	SYMBOLS	SKFM 1040CW-D2	SKFM 1045CW-D2	SKFM 1060CW-D2	SKFM 10100CW-D2	SKFM 10150CW-D2	SKFM 10200CW-D2	UNIT
Typical thermal resistance junction to case per leg	$R_{\theta JC}$	3.5						$^\circ\text{C/W}$

Rating and characteristic curves (SKFM1040CW-D2 THRU SKFM10200CW-D2)

FIG.1-DERATING CURVE OUTPUT RECTIFIED CURRENT

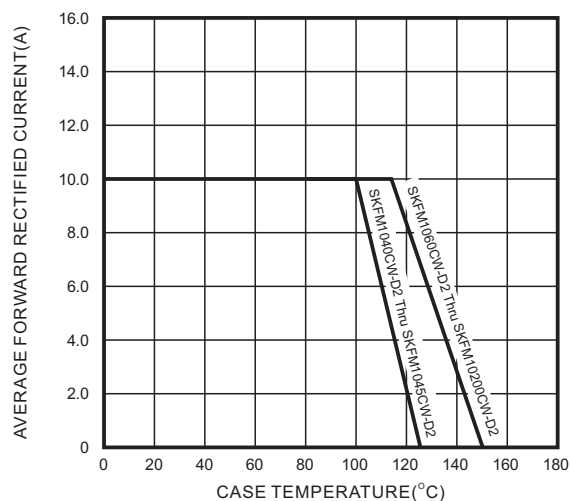


FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

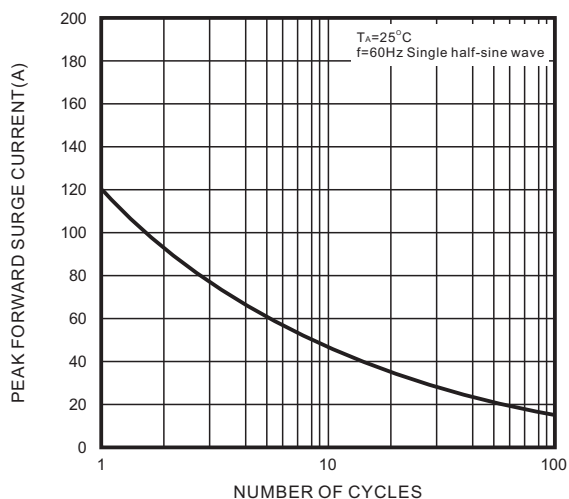


FIG.3-TYPICAL REVERSE LEAKAGE CHARACTERISTICS

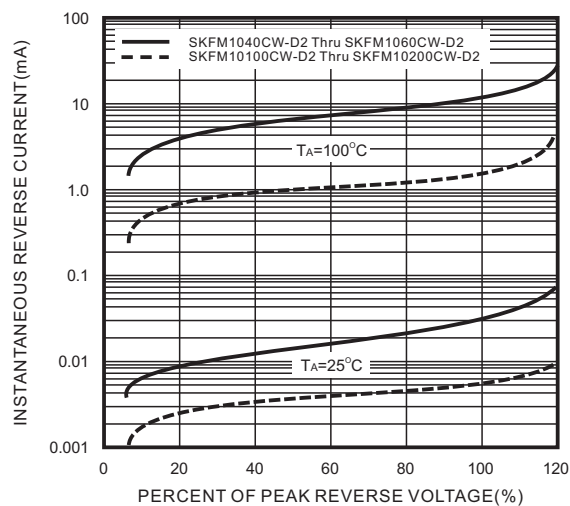
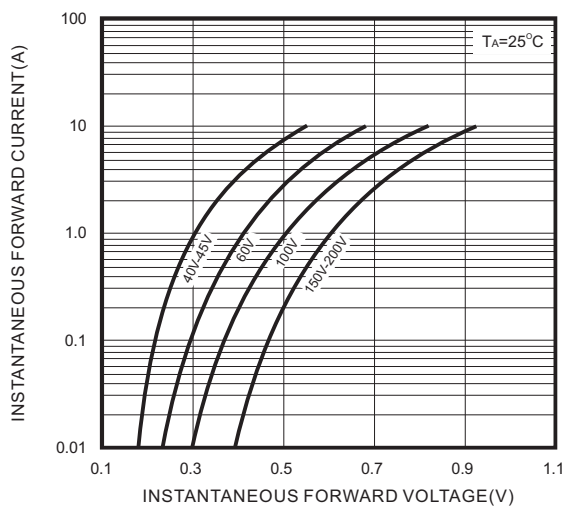
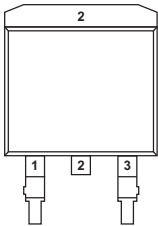
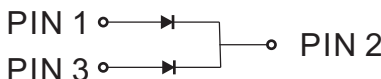


FIG.4-TYPICAL FORWARD VOLTAGE CHARACTERISTICS



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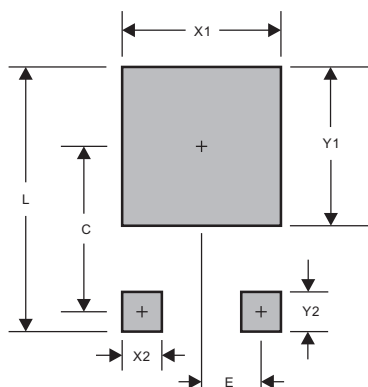
Pinning information

Pin	Simplified outline	Symbol
Pin1 anode Pin2 cathode Pin3 anode		

Marking

Type number	Marking code
SKFM1040CW-D2	MBR1040CT
SKFM1045CW-D2	MBR1045CT
SKFM1060CW-D2	MBR1060CT
SKFM10100CW-D2	MBR10100CT
SKFM10150CW-D2	MBR10150CT
SKFM10200CW-D2	MBR10200CT

Suggested solder pad layout

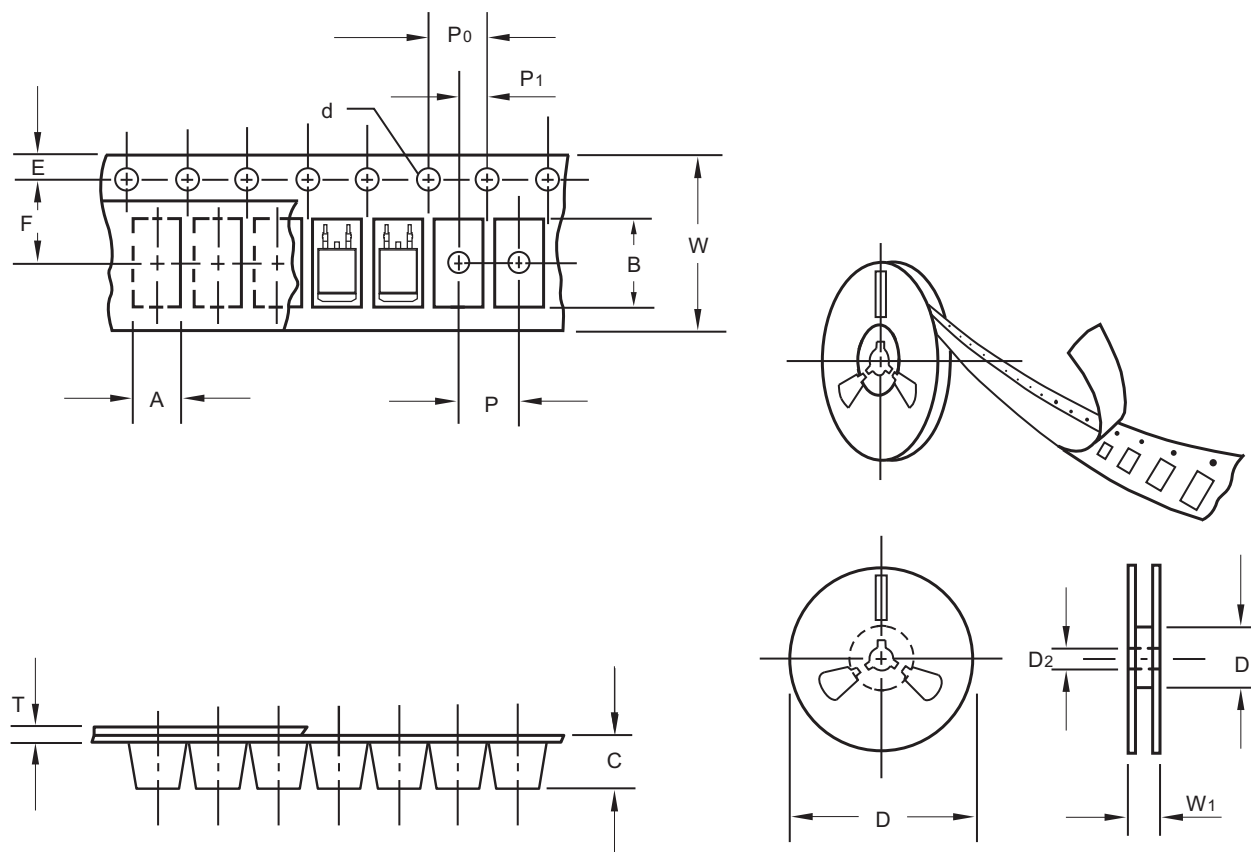


PACKAGE	D2PAK-1
C	0.374(9.50)
E	0.098(2.50)
L	0.665(16.90)
X1	0.425(10.80)
X2	0.071(1.80)
Y1	0.449(11.40)
Y2	0.138(3.50)

Dimensions in inches and (millimeters)

SKFM1040CW-D2 THRU SKFM10200CW-D2

Packing information



unit:mm

Item	Symbol	Tolerance	D2PAK-1
Carrier width	A	0.1	10.70
Carrier length	B	0.1	16.30
Carrier depth	C	0.1	5.10
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	330.00
13" Reel inner diameter	D1	min	50.00
7" Reel outside diameter	D	2.0	-
7" Reel inner diameter	D1	min	-
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	11.50
Punch hole pitch	P	0.1	16.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	24.00
Reel width	W1	1.0	30.00

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

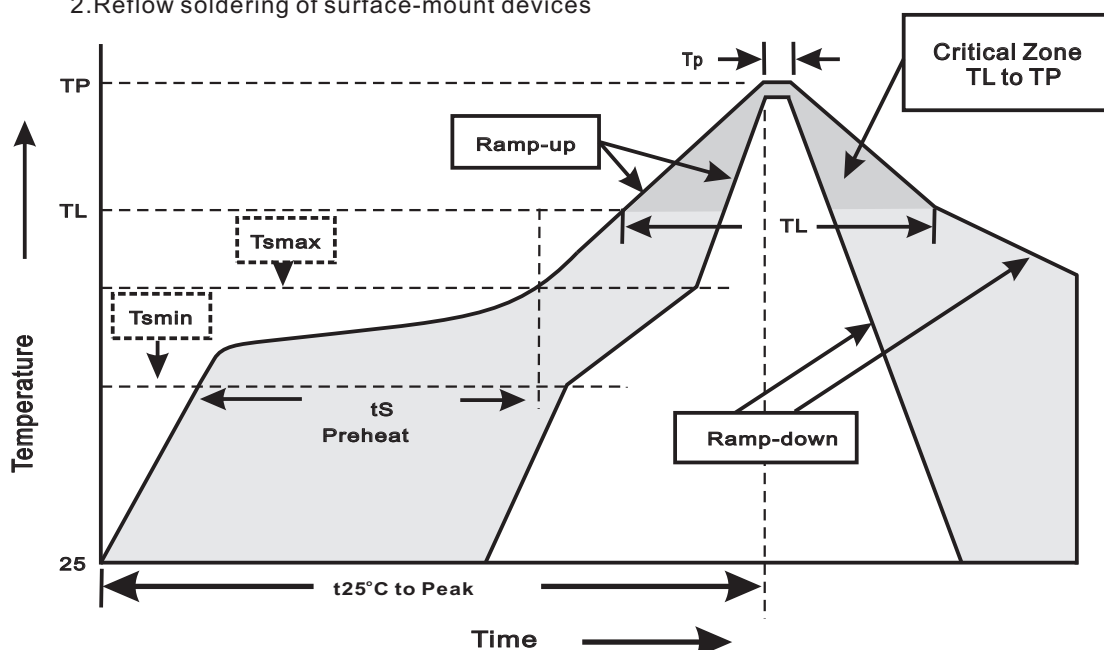
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Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
D2PAK-1/ TO-263	13"	800	16.0	800	340*340*38	330	360*360*360	6,400	15.0

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{Smin}) -Temperature Max(T _{Smax}) -Time(min to max)(t _S)	150°C 200°C 60~120sec
T _{Smax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<3°C/sec
Time 25°C to Peak Temperature	<6minutes

SKFM1040CW-D2 THRU SKFM10200CW-D2**High reliability test capabilities**

Item Test	Conditions	Reference
1. Solder Resistance	at $260 \pm 5^\circ\text{C}$ for 10 ± 2 sec.	MIL-STD-750D METHOD-2031
2. Solderability	at $245 \pm 5^\circ\text{C}$ for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_R = 80\%$ rate at $T_J = 125^\circ\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Forward Operation Life	Rated average rectifier current at $T_A = 25^\circ\text{C}$ for 500hrs.	MIL-STD-750D METHOD-1027
5. Intermittent Operation Life	$T_A = 25^\circ\text{C}$, $I_F = I_O$ On state: power on for 5 min. off state: power off for 5 min. on and off for 500 cycles.	MIL-STD-750D METHOD-1036
6. Pressure Cooker	$15P_{SIG}$ at $T_A = 121^\circ\text{C}$ for 4 hrs.	JESD22-A102
7. Temperature Cycling	-55°C to $+125^\circ\text{C}$ dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
8. Forward Surge	8.3ms single half sine-wave , one surge.	MIL-STD-750D METHOD-4066-2
9. Humidity	at $T_A = 85^\circ\text{C}$, RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
10. High Temperature Storage Life	at 175°C for 1000 hrs.	MIL-STD-750D METHOD-1031