

# FM240-MST THRU FM2200-MST

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# FM240-MST THRU FM2200-MST

## 2.0A Surface Mount Schottky Barrier Rectifiers 40V-200V

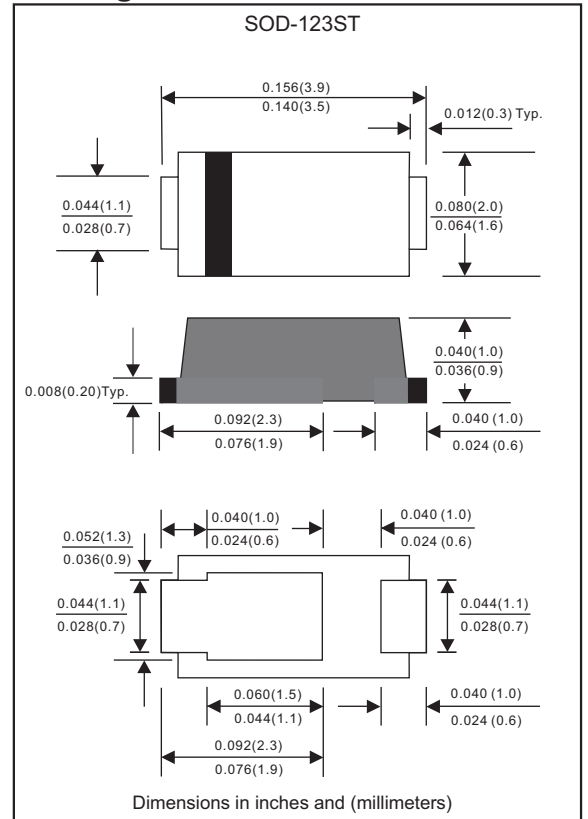
### Features

- Well package design with solder pad on the bottom for best thermal performance
- Low profile surface mounted application in order to optimize board space
- Tiny plastic SMD package
- Low power loss, high efficiency
- High current capability, low forward voltage drop
- High surge capability
- Silicon epitaxial planar chip, metal silicon junction
- Lead-free parts meet RoHS requirements
- Suffix "-H" indicates Halogen free parts, ex. FM240-MST-H

### Mechanical data

- Epoxy : UL94-V0 rated flame retardant
- Case : Molded plastic, SOD-123ST / MINI SMA
- Terminals :Plated terminals, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.0155 gram

### Package outline



### Maximum ratings (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	FM240-MST	FM260-MST	FM2100-MST	FM2150-MST	FM2200-MST	UNITS
Maximum repetitive peak reverse voltage	$V_{RRM}$	40	60	100	150	200	V
Maximum RMS voltage	$V_{RMS}$	28	42	70	105	140	V
Maximum continuous reverse voltage	$V_R$	40	60	100	150	200	V
Maximum average forward rectified current	$I_o$	2.0					A
Non-repetitive peak forward surge current 8.3ms single half sine-wave	$I_{FSM}$	50					A
Typical junction capacitance (Note 1)	$C_J$	110	80	60	45	35	pF
Operating junction temperature range	$T_J$	-55 to +125		-55 to +150			$^{\circ}\text{C}$
Storage temperature range	$T_{STG}$	-65 to +175					$^{\circ}\text{C}$

### Electrical characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	FM240-MST	FM260-MST	FM2100-MST	FM2150-MST	FM2200-MST	UNITS
Maximum instantaneous forward voltage at $I_F=2.0\text{A}$	$V_F$	0.50	0.70	0.85	0.90	0.92	Volts
Maximum reverse leakage current at rated $V_R$	$I_R$	0.5 20					mA mA

### Thermal characteristics

PARAMETER	SYMBOLS	FM240-MST	FM260-MST	FM2100-MST	FM2150-MST	FM2200-MST	UNITS
Typical thermal resistance junction to ambient (Note 2)	$R_{\theta JA}$	70					$^{\circ}\text{C} / \text{W}$
Typical thermal resistance junction to case (Note 2)	$R_{\theta JC}$	30					$^{\circ}\text{C} / \text{W}$

Notes1: Measured at 1MHz and applied reverse voltage of 4.0V D.C  
2: Mounted on FR-4 PCB copper, minimum recommended pad layout

# Rating and characteristic curves (FM240-MST THRU FM2200-MST)

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

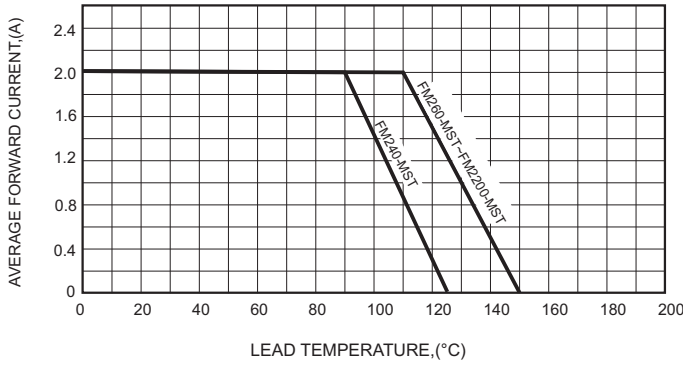


FIG.2-TYPICAL FORWARD CHARACTERISTICS

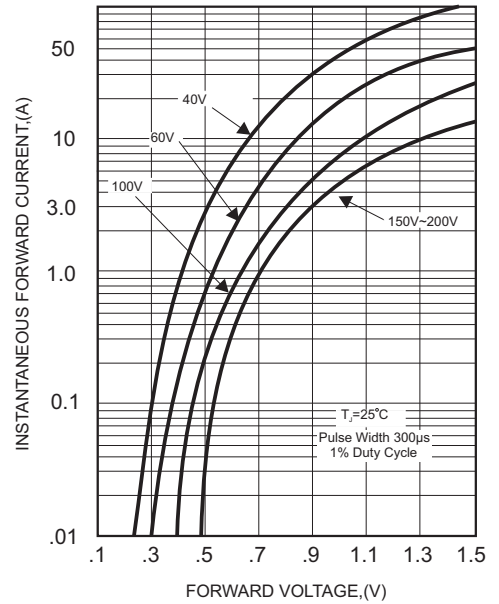


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

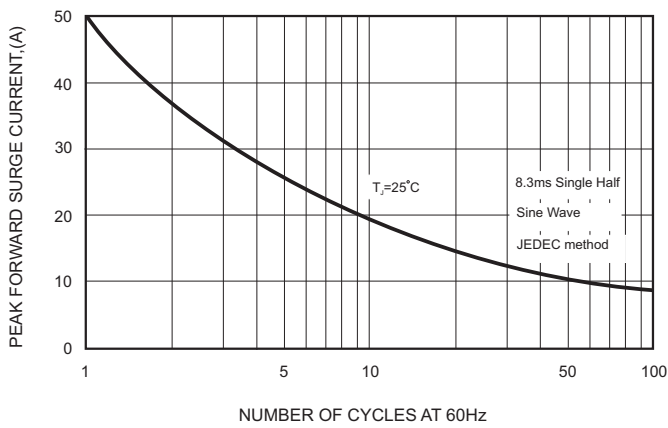


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

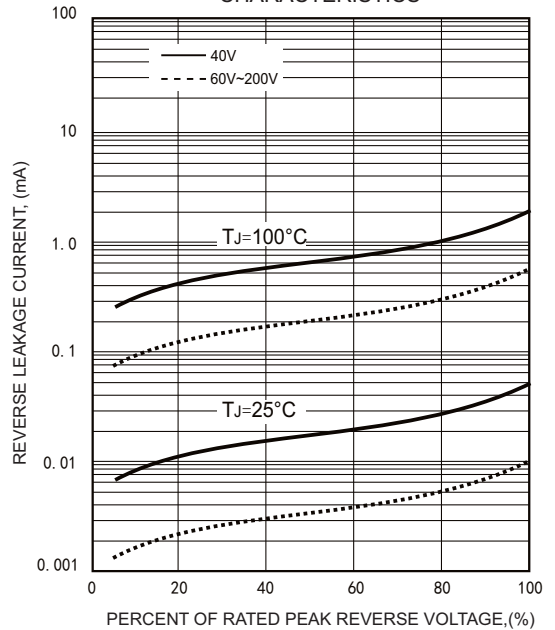
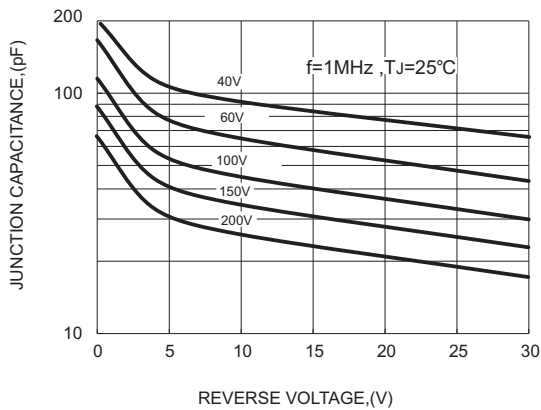




FIG.4-TYPICAL JUNCTION CAPACITANCE



# FM240-MST THRU FM2200-MST

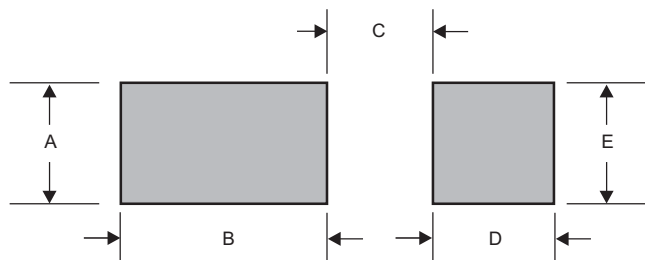
## Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

## Marking

Type number	Marking code
FM240-MST	24
FM260-MST	26
FM2100-MST	20
FM2150-MST	215
FM2200-MST	220

## Suggested solder pad layout

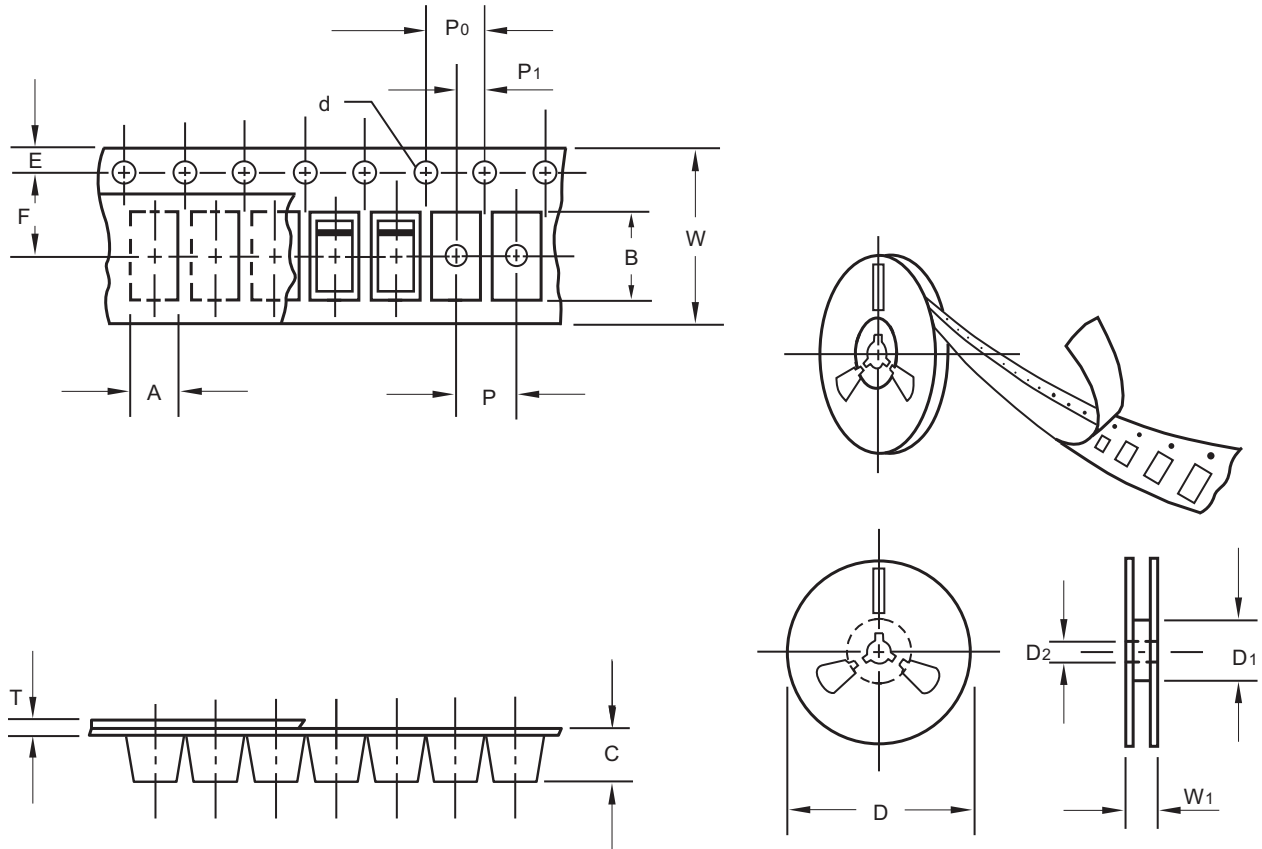


Dimensions in inches and (millimeters)

PACKAGE	A	B	C	D	E
SOD-123ST	0.060 (1.50)	0.095 (2.40)	0.020 (0.50)	0.044 (1.10)	0.049 (1.25)

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## Packing information



unit:mm

Item	Symbol	Tolerance	SOD-123ST
Carrier width	A	0.1	2.00
Carrier length	B	0.1	3.85
Carrier depth	C	0.1	1.10
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	-
13" Reel inner diameter	D1	min	-
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	62.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	11.40

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

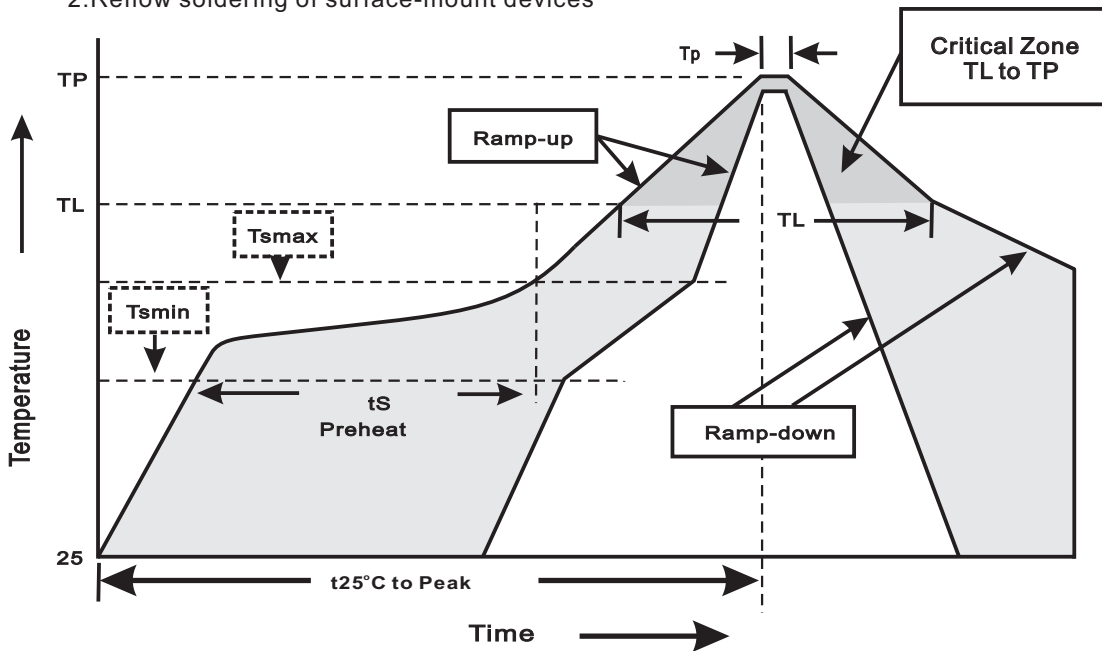
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## Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOD-123ST	7"	3,000	4.0	30,000	183*123*183	178	382*257*387	240,000	9.5

## Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



### 3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(TL to TP)	<3°C/sec
Preheat -Temperature Min(Tsmin) -Temperature Max(Tsmax) -Time(min to max)(ts)	150°C 200°C 60~120sec
Tsmax to TL -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(TL) -Time(tL)	217°C 60~260sec
Peak Temperature(TP)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(tp)	10~30sec
Ramp-down Rate	<3°C/sec
Time 25°C to Peak Temperature	<6minutes

**FM240-MST THRU FM2200-MST****High reliability test capabilities**

Item Test	Conditions	Reference
1. Solder Resistance	at 260±5°C for 10±2sec.	MIL-STD-750D METHOD-2031
2. Solderability	at 245±5°C for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_R=80\%$ rate at $T_J=125^\circ\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Forward Operation Life	Rated average rectifier current at $T_A=25^\circ\text{C}$ for 500hrs.	MIL-STD-750D METHOD-1027
5. Intermittent Operation Life	$T_A = 25^\circ\text{C}$ , $I_F = I_O$ On state: power on for 5 min. off state: power off for 5 min. on and off for 500 cycles.	MIL-STD-750D METHOD-1036
6. Pressure Cooker	15P <sub>SIG</sub> at $T_A=121^\circ\text{C}$ for 4 hrs.	JESD22-A102
7. Temperature Cycling	-55°C to +125°C dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
8. Forward Surge	8.3ms single half sine-wave , one surge.	MIL-STD-750D METHOD-4066-2
9. Humidity	at $T_A=85^\circ\text{C}$ , RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
10. High Temperature Storage Life	at 175°C for 1000 hrs.	MIL-STD-750D METHOD-1031