

FMSNN3406-H

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3.6A 30V N-Channel Trench Enhancement Mode MOSFET

Features

- $V_{DS}=30V, I_D=3.6A$.
- $R_{DS(ON)} \leq 65m\Omega, @V_{GS}=10V, I_D=3.6A$.
- $R_{DS(ON)} \leq 105m\Omega, @V_{GS}=4.5V, I_D=2.8A$.
- Provide excellent on-resistance and low gate charge.
- Lead-free parts meet RoHS requirements
- Halogen-free (IEC61249-2-21).

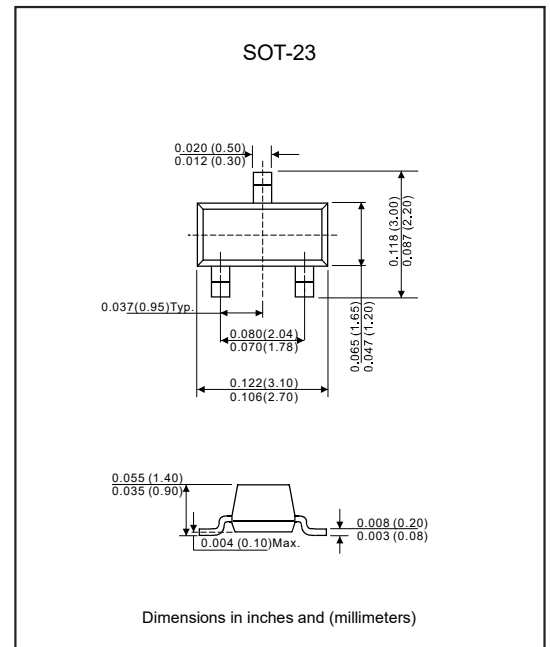
Application

- Load switching and PWM applications.

Mechanical data

- Epoxy:UL94-V0 rated flame retardant.
- Case : Molded plastic, SOT-23.
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026.
- Mounting Position : Any.

Package outline



Maximum Ratings (At $T_a=25^\circ C$ Unless otherwise noted)

| Parameter | Symbol | Rating | Unit |
|---|-----------------|-------------|--------------|
| Drain source voltage | V_{DS} | 30 | V |
| Gate source voltage | V_{GS} | ± 20 | V |
| Drain current continuous | I_D | 3.6 | A |
| Pulsed drain current (Note1) | I_{DM} | 15 | A |
| Power dissipation | P_D | 0.35 | W |
| Thermal resistance, junction to ambient | $R_{\theta JA}$ | 357 | $^\circ C/W$ |
| Operating junction temperature | T_J | +150 | $^\circ C$ |
| Storage temperature range | T_{STG} | -50 to +150 | $^\circ C$ |

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Electrical characteristics (At $T_A=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
|---|--------------|---|-----|------|-----------|---------------|
| Off characteristics | | | | | | |
| Drain-Source breakdown voltage | BV_{DSS} | $I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$ | 30 | | | V |
| Drain-Source leakage current | I_{DSS} | $V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$ | | | 1 | μA |
| Gate-Source leakage current | I_{GSS} | $V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$ | | | ± 100 | nA |
| On characteristics | | | | | | |
| Gate threshold voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$ | 1 | | 3 | V |
| Static drain-source on-resistance (Note2) | $R_{DS(on)}$ | $V_{GS}=10\text{V}$, $I_D=3.6\text{A}$ | | 40 | 65 | m Ω |
| | | $V_{GS}=4.5\text{V}$, $I_D=2.8\text{A}$ | | 72 | 105 | |
| Forward transconductance (Note2) | g_{FS} | $V_{DS}=5\text{V}$, $I_D=3.6\text{A}$ | 3 | | | S |
| Dynamic characteristics (Note3) | | | | | | |
| Input capacitance | C_{iss} | $V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$ | | | 375 | pF |
| Output capacitance | C_{oss} | | | 57 | | |
| Reverse transfer capacitance | C_{rss} | | | 39 | | |
| Switching characteristics (Note3) | | | | | | |
| Turn-on delay time | $t_{d(on)}$ | $V_{DS}=15\text{V}$, $V_{GS}=10\text{V}$, $R_G=3\Omega$, $R_L=2.2\Omega$ | | 4.6 | | ns |
| Turn-on rise time | t_r | | | 1.9 | | |
| Turn-off delay time | $t_{d(off)}$ | | | 20.1 | | |
| Turn-off fall time | t_f | | | 2.6 | | |
| Drain-Source diode characteristics and maximum ratings | | | | | | |
| Drain-Source diode forward voltage | V_{SD} | $I_S=1\text{A}$ | | | 1 | V |

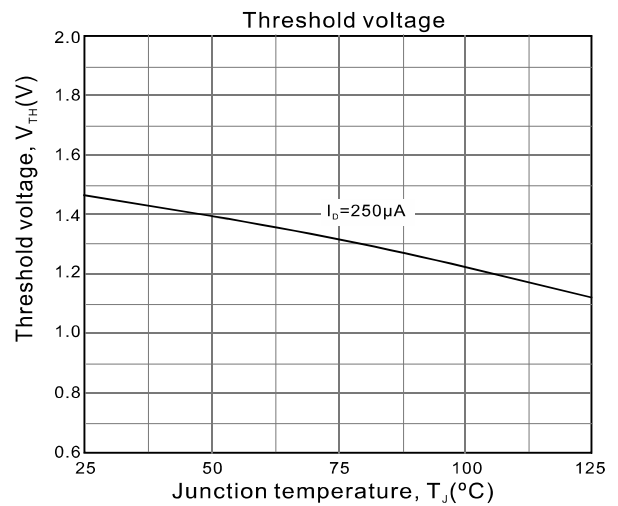
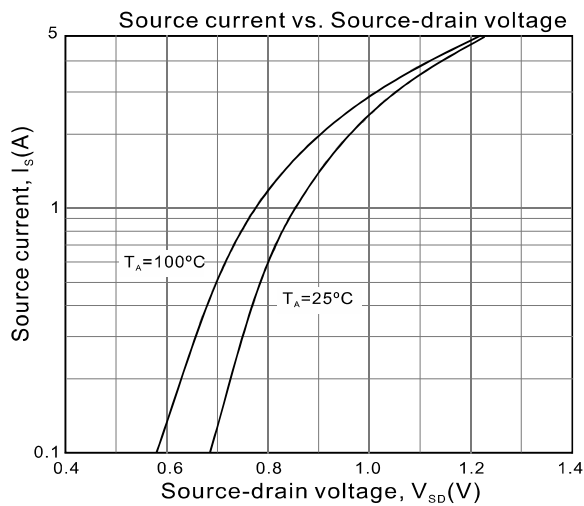
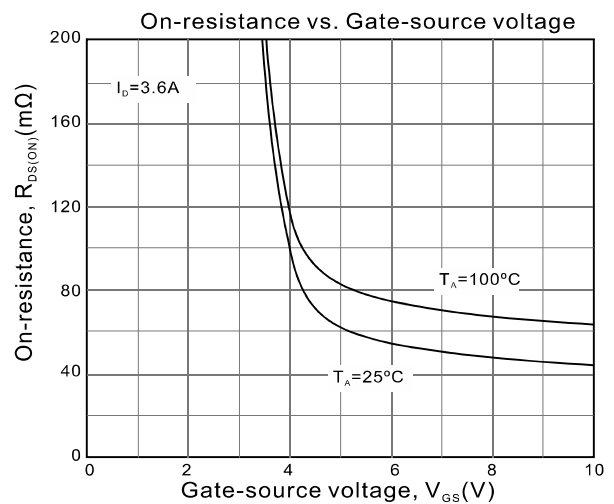
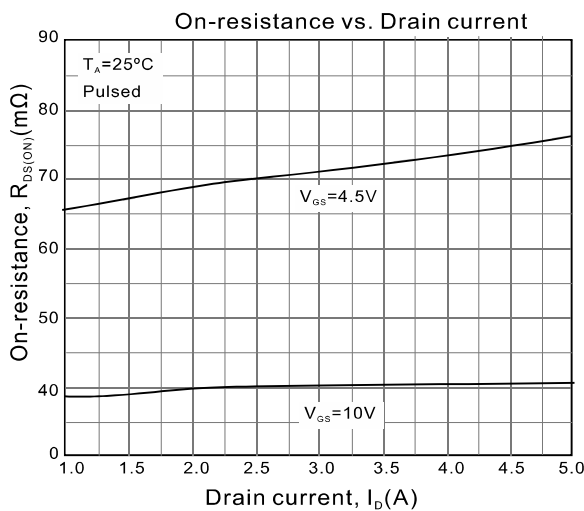
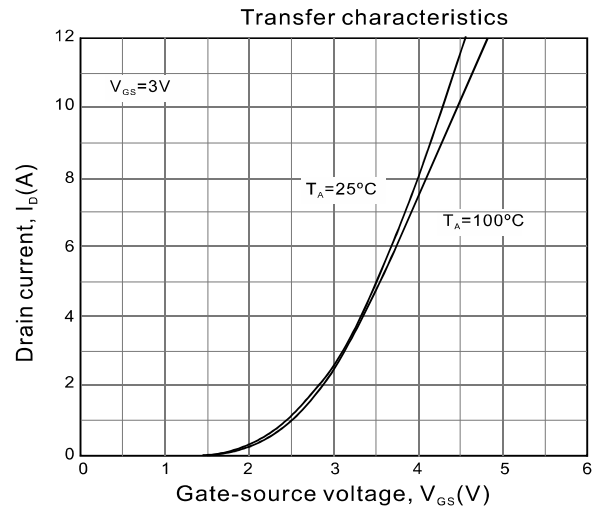
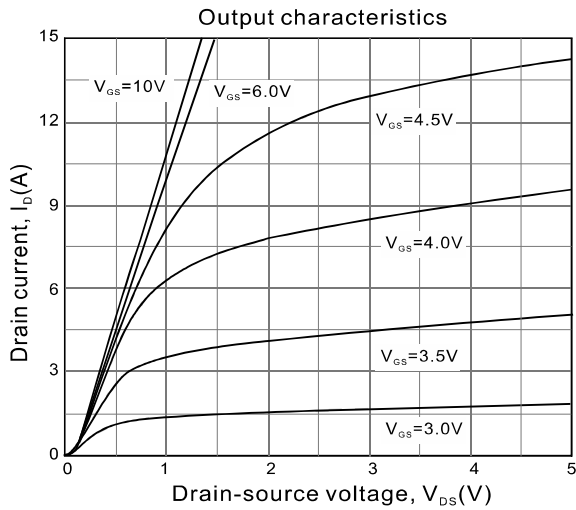
Note: 1. Repetitive rating: pulse width limited by maximum junction temperature.

2. Pulsed test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 0.5\%$.

3. These parameters have no way to verify.

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Rating and characteristic curves



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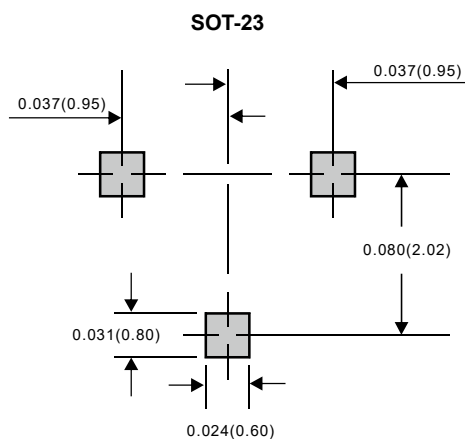
Pinning information

| Pin | Simplified outline | Symbol |
|---|--------------------|--------|
| Pin 1 Gate Pin 2 Source Pin 3 Drain | | |

Marking

| Type number | Marking code |
|-------------|--------------|
| FMSNN3406-H | R6 |

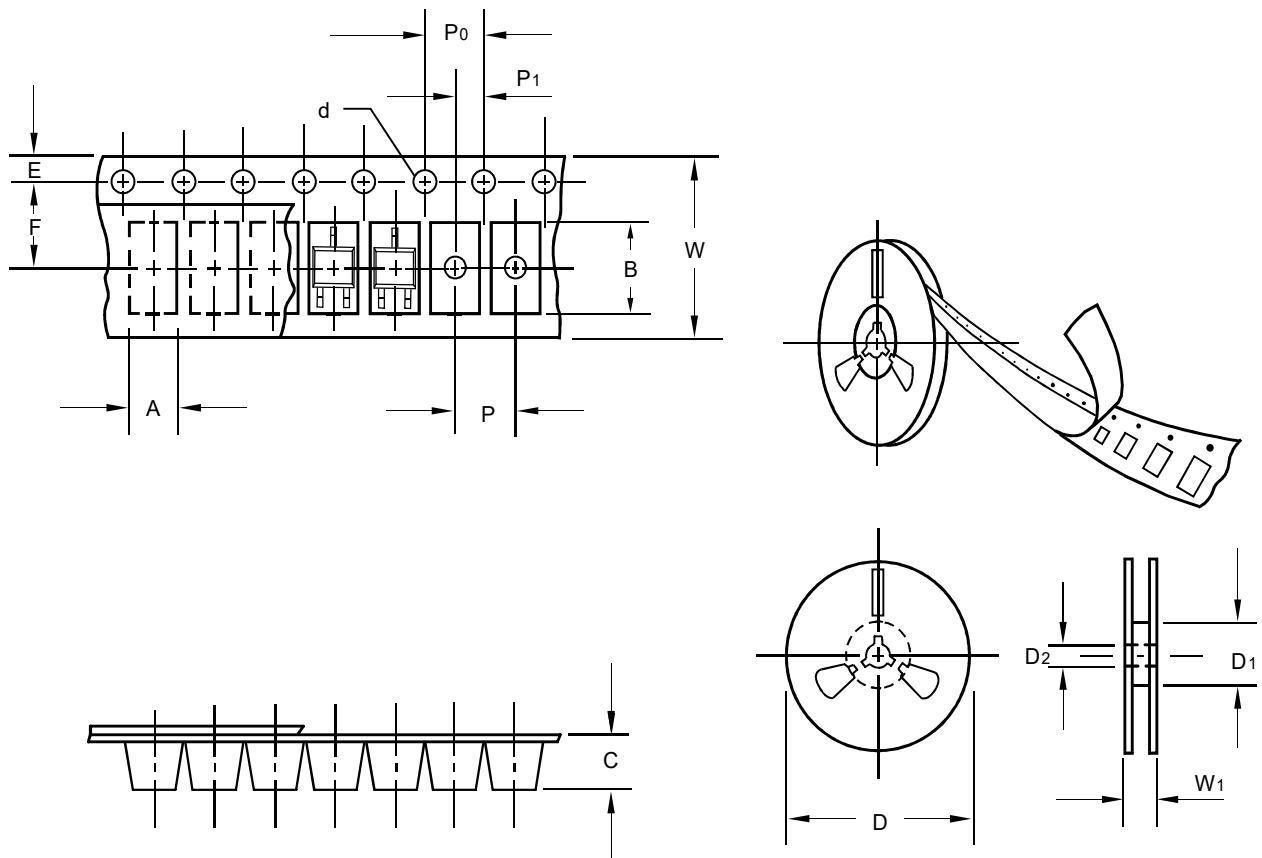
Suggested solder pad layout



Dimensions in inches and (millimeters)

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Packing information



unit:mm

| Item | Symbol | Tolerance | SOT-23 |
|---------------------------|----------------|-----------|--------|
| Carrier width | A | 0.1 | 3.15 |
| Carrier length | B | 0.1 | 2.77 |
| Carrier depth | C | 0.1 | 1.22 |
| Sprocket hole | d | 0.1 | 1.50 |
| 13" Reel outside diameter | D | 2.0 | - |
| 13" Reel inner diameter | D ₁ | min | - |
| 7" Reel outside diameter | D | 2.0 | 178.00 |
| 7" Reel inner diameter | D ₁ | min | 54.40 |
| Feed hole diameter | D ₂ | 0.5 | 13.00 |
| Sprocket hole position | E | 0.1 | 1.75 |
| Punch hole position | F | 0.1 | 3.50 |
| Punch hole pitch | P | 0.1 | 4.00 |
| Sprocket hole pitch | P ₀ | 0.1 | 4.00 |
| Embossment center | P ₁ | 0.1 | 2.00 |
| Tape width | W | 0.1 | 8.00 |
| Reel width | W ₁ | 0.1 | 12.30 |

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

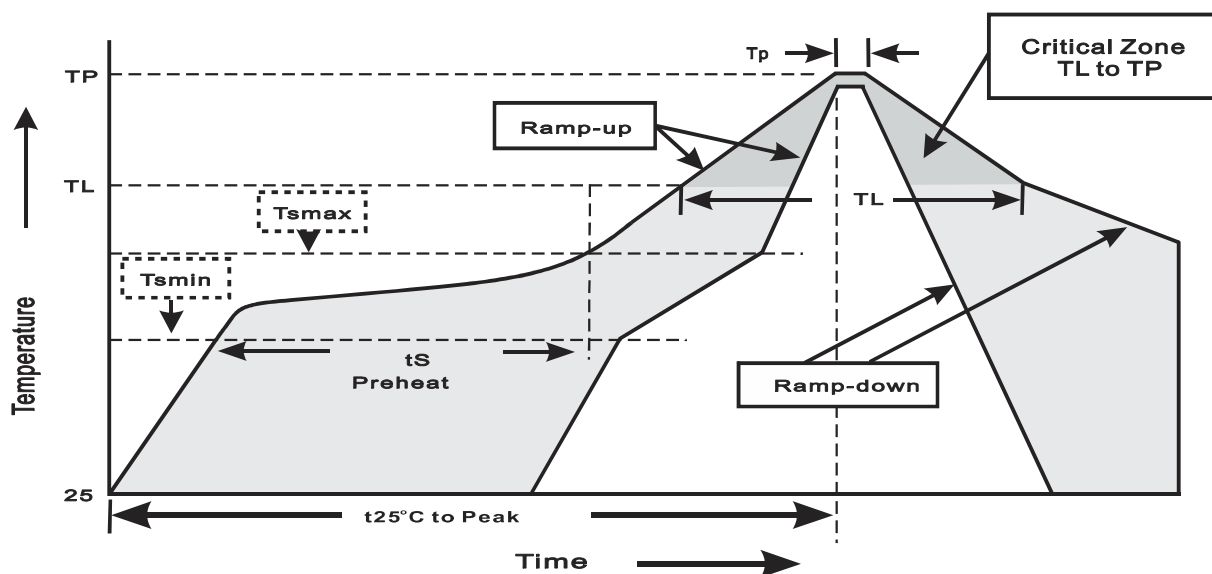
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Reel packing

| PACKAGE | REEL SIZE | REEL (pcs) | COMPONENT SPACING (m/m) | BOX (pcs) | INNER BOX (m/m) | REEL DIA. (m/m) | CARTON SIZE (m/m) | CARTON (pcs) |
|---------|-----------|------------|-------------------------|-----------|-----------------|-----------------|-------------------|--------------|
| SOT-23 | 7" | 3,000 | 4.0 | 30,000 | 183*123*183 | 178 | 382*257*387 | 240,000 |

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

| Profile Feature | Soldering Condition |
|--|-----------------------------|
| Average ramp-up rate(TL to TP) | <3°C/sec |
| Preheat -Temperature Min(Tsmin) -Temperature Max(Tsmax) -Time(min to max)(ts) | 150°C 200°C 60~120sec |
| Tsmax to TL -Ramp-upRate | <3°C/sec |
| Time maintained above: -Temperature(TL) -Time(tL) | 217°C 60~260sec |
| Peak Temperature(TP) | 255°C-0/+5°C |
| Time within 5°C of actual Peak Temperature(tp) | 10~30sec |
| Ramp-down Rate | <6°C/sec |
| Time 25°C to Peak Temperature | <6minutes |