

FMOSK2605-H

List

List.....1

Package outline.....2

Features.....2

Applications.....2

Mechanical data.....2

Maximum ratings2

Electrical characteristics.....3

Rating and characteristic curves.....4~5

Pinning information.....6

Marking.....6

Suggested solder pad layout.....6

Packing information.....7

Suggested thermal profiles for soldering processes.....8

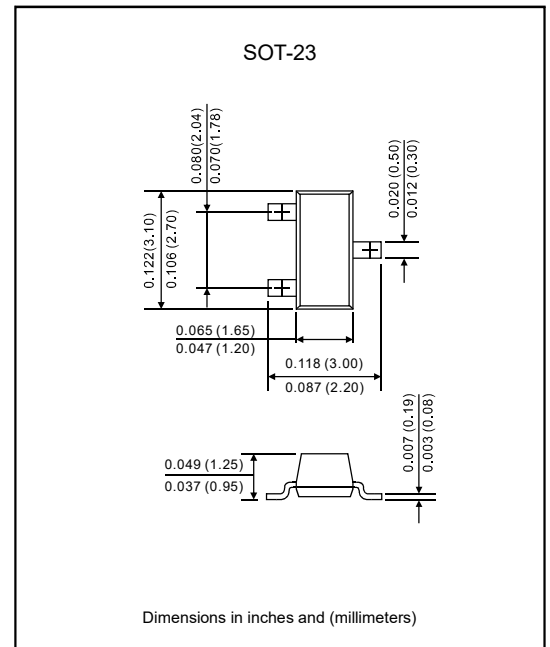
FMOSK2605-H

**-6A -30V P-Channel
Enhancement Mode Trench MOSFET**
Features

- $-V_{DS}=30V$, $-I_D=6A$.
- $R_{DS(ON)} \leq 32m\Omega$, @ $-V_{GS}=10V$, $-I_D=4.2A$.
- $R_{DS(ON)} \leq 58m\Omega$, @ $-V_{GS}=4.5V$, $-I_D=4.0A$.
- $R_{DS(ON)} \leq 78m\Omega$, @ $-V_{GS}=2.5V$, $-I_D=1.0A$.
- Super high dense cell trench design for low on-resistance.
- Rugged and reliable.
- Lead-free parts meet RoHS requirements.

Mechanical data

- Epoxy:UL94-V0 rated flame retardant.
- Case : Molded plastic, SOT-23.
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026.
- Mounting Position : Any.

Package outline**Maximum ratings** (At $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Ratings	Units
Drain-source voltage	$-V_{DS}$	30	V
Gate-source voltage	V_{GS}	± 20	V
Continuous drain current (Note 1)	$-I_D$	6	A
Pulsed drain current (Note 2)	$-I_{DM}$	24	A
Drain-source diode forward current (Note 1)	$-I_S$	2.3	A
Power dissipation (Note 1)	P_D	1.25	W
Thermal resistance, junction to ambient (Note 1)	$R_{\theta JA}$	100	$^\circ C/W$
Operation junction temperature	T_J	+150	$^\circ C$
Storage temperature range	T_{STG}	-55 to +150	$^\circ C$

FMOSK2605-H

Electrical characteristics (At $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Condition	Symbol	Min	Typ	Max	Units
Off characteristics						
Drain-source breakdown voltage	$V_{GS}=0V, -I_D=250A$	$-BV_{DS}$	30			V
Zero gate voltage drain current	$-V_{DS}=20V, V_{GS}=0V$	$-I_{DSS}$			1	μA
Gate-body leakage current	$V_{GS}=\pm 16V, V_{DS}=0V$	I_{GSS}			± 100	nA

On characteristics (Note2)

Gate threshold voltage	$V_{DS}=V_{GS}, -I_D=250\mu A$	$-V_{GS(th)}$	1	1.5	2	V
Drain-source on-state resistance	$-V_{GS}=10V, -I_D=4.2A$	$R_{DS(on)}$		25	32	m Ω
	$-V_{GS}=4.5V, -I_D=4.0A$			43	58	
	$-V_{GS}=2.5V, -I_D=1.0A$			65	78	

Dynamic parameters (Note3)

Input capacitance	$V_{GS}=0V, -V_{DS}=15V, f=1.0MHz$	C_{iss}		987		pF
Output capacitance		C_{oss}		246		
Reverse transfer capacitance		C_{rss}		110		

Switching parameters (Note3)

Total gate charge	$-V_{GS}=10V, -V_{DS}=15V, -I_D=1A$	Q_g		18.2		nC
Gate-source charge		Q_{gs}		4.2		
Gate-drain charge		Q_{gd}		5.1		
Turn-on delay time	$-V_{DD}=15V, -I_D=1A, -V_{GEN}=4.5V, R_L=15\Omega, R_{GEN}=10\Omega$	$t_{d(on)}$		38		ns
Turn-on rise time		t_r		4.2		
Turn-off delay time		$t_{d(off)}$		13		
Turn-off fall time		t_f		7.8		

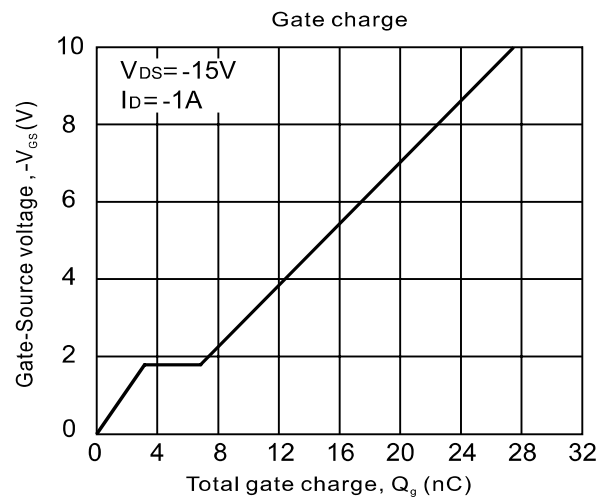
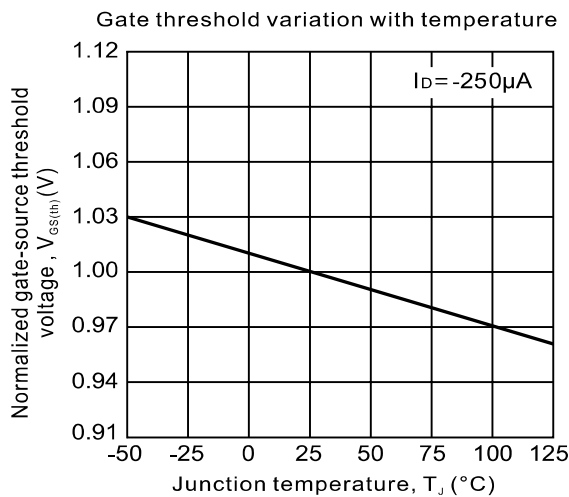
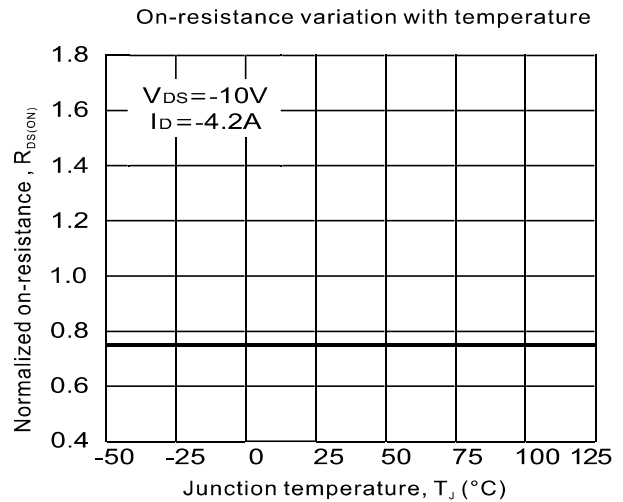
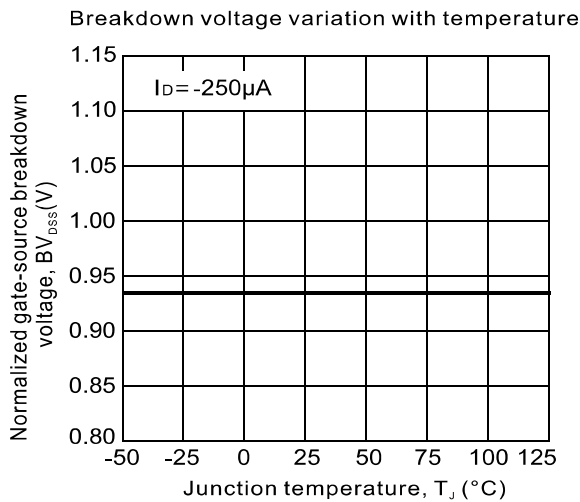
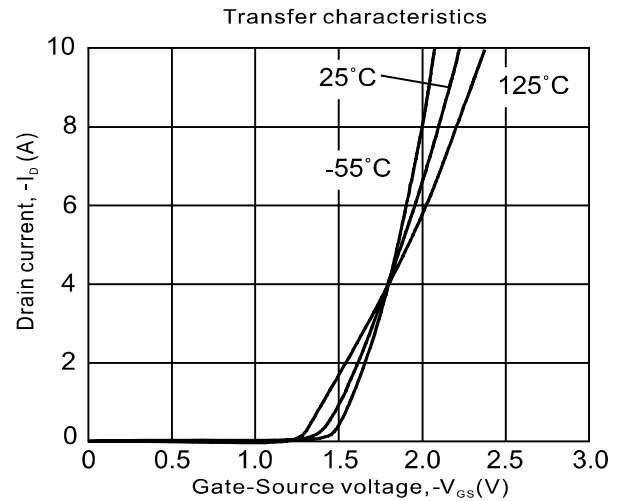
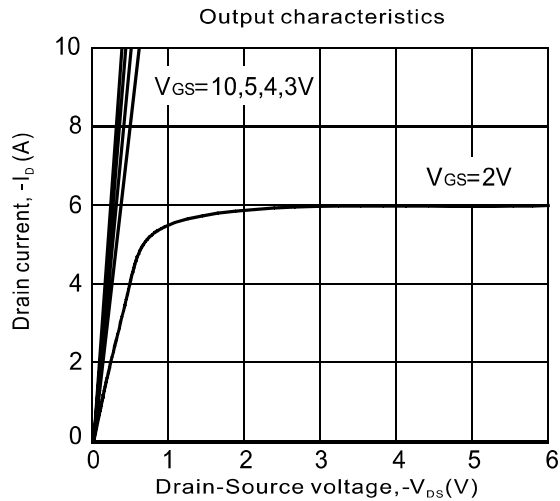
Source-drain diode ratings and characteristics

Drain-source diode forward voltage (Note 2)	$-I_S=1A, V_{GS}=0V$	$-V_{SD}$			1.0	V
---	----------------------	-----------	--	--	-----	---

Note: 1. Surface mounted on FR4 board, $t \leq 5$ seconds.
2. Pulse test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. Guaranteed by design, not subject to production testing.

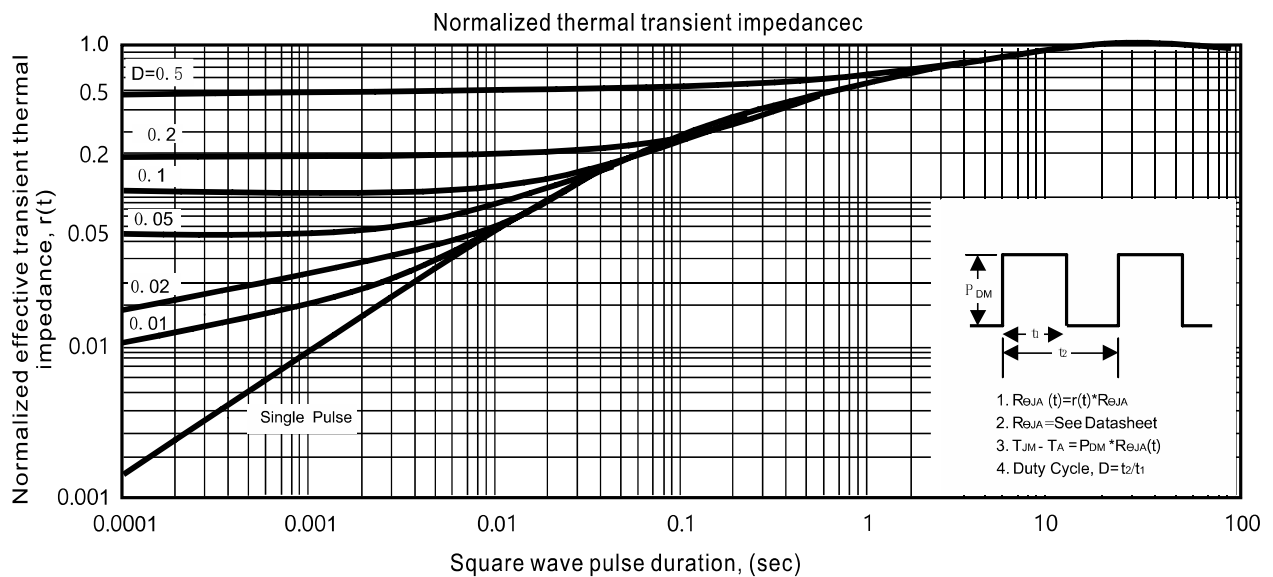
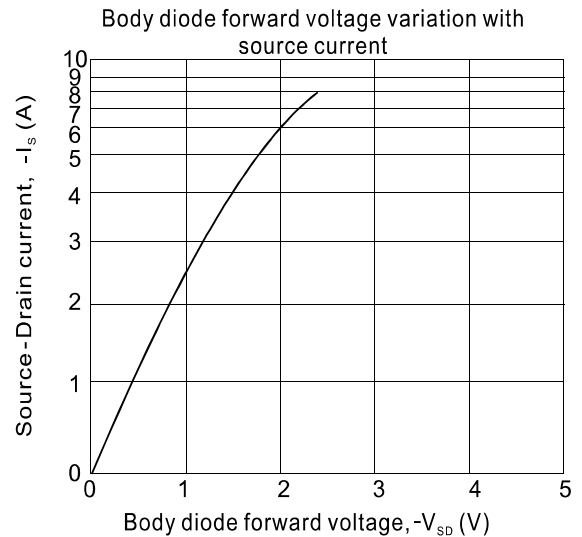
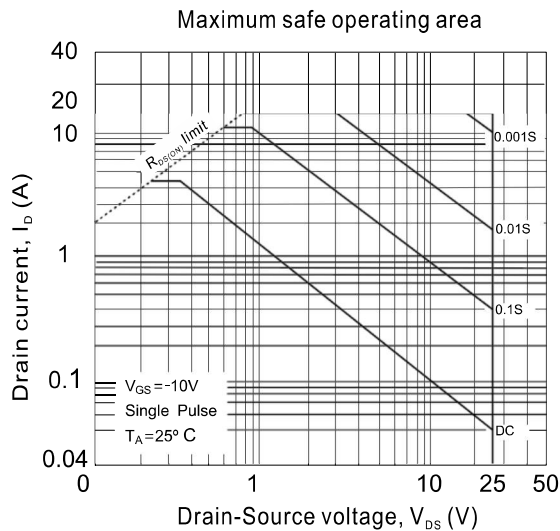
FMOSK2605-H

Rating and characteristic curves



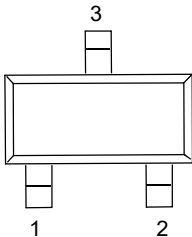
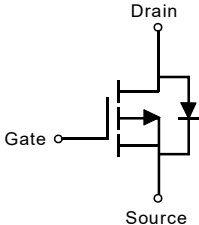
FMOSK2605-H

Rating and characteristic curves



FMOSK2605-H

Pinning information

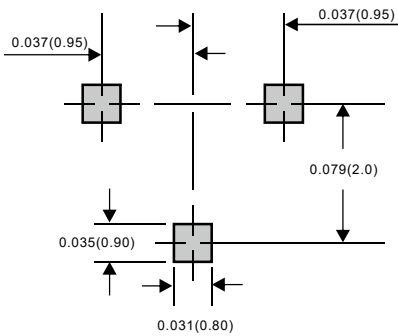
Pin	Simplified outline	Symbol
Pin 1 Gate Pin 2 Source Pin 3 Drain		

Marking

Type number	Marking code
FMOSK2605-H	K2605

Suggested solder pad layout

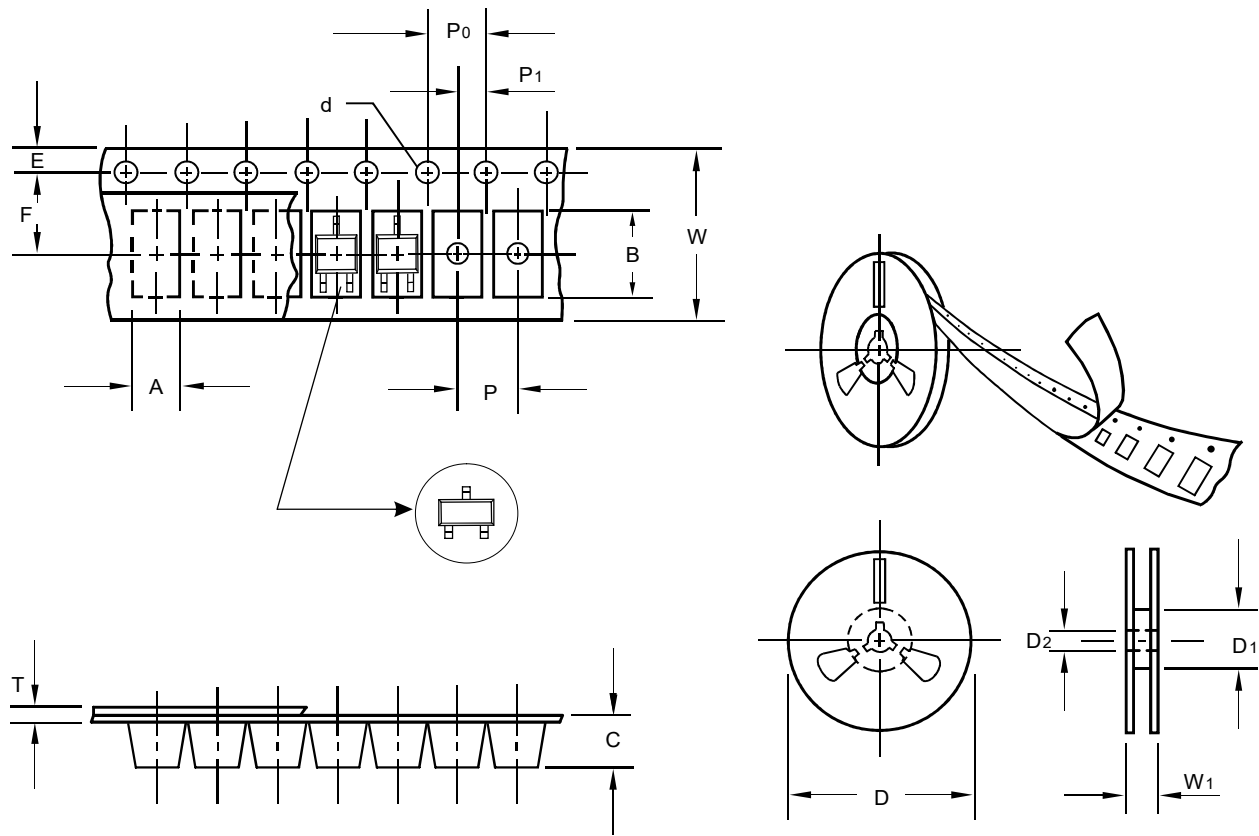
SOT-23



Dimensions in inches and (millimeters)

FMOSK2605-H

Packing information



unit:mm

Item	Symbol	Tolerance	SOT-23
Carrier width	A	0.1	3.15
Carrier length	B	0.1	2.77
Carrier depth	C	0.1	1.22
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	-
13" Reel inner diameter	D1	min	-
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	54.40
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	12.3

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

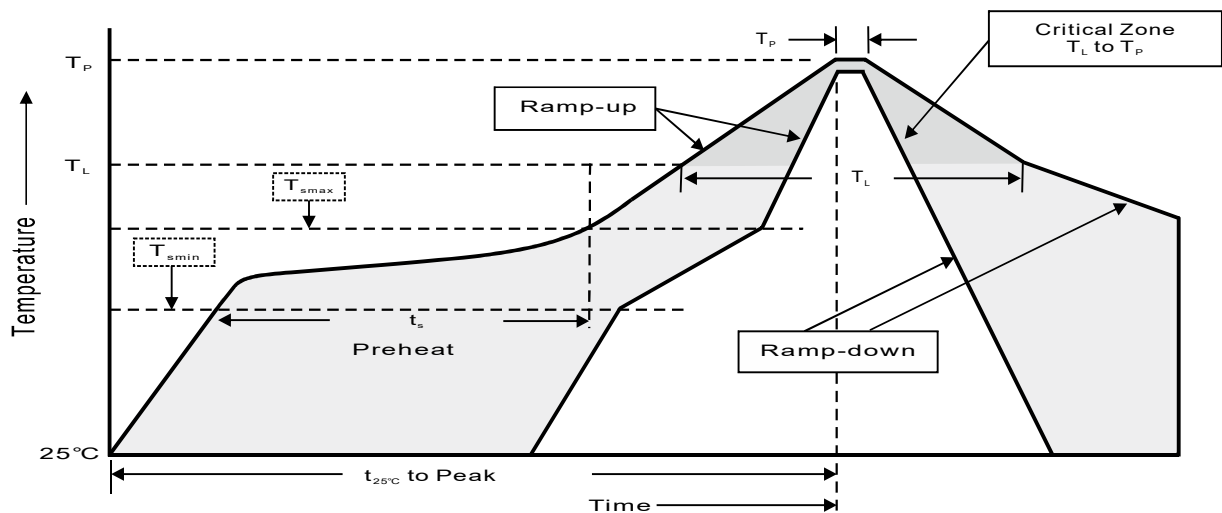
FMOSK2605-H

Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)
SOT-23	7"	3,000	4.0	30,000	183*123*183	178	382*257*387	240,000

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature = 5°C ~ 40°C Humidity = 55%, ±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile feature	Soldering condition
Average ramp-up rate (T_L to T_p)	< 3 °C/sec
Preheat - Temperature Min (T_{smin}) - Temperature Max (T_{smax}) - Time (Min to Max) (t_s)	150°C 200°C 60 ~ 120 sec
T_{smax} to T_L - Ramp-up rate	< 3 °C/sec
Time maintained above : - Temperature (T_L) - Time (T_L)	217°C 60 ~ 260 sec
Peak temperature (T_p)	255 °C -0/+5°C
Time with 5°C of actual peak temperature (T_p)	10 ~30 sec
Ramp-down rate	< 6 °C/sec
Time 25°C to peak temperature	< 6 minutes