

FMOSAC3080

List

List.....1

Package outline.....2

Features.....2

Applications.....2

Mechanical data.....2

Maximum ratings2

Electrical characteristics.....3

Rating and characteristic curves.....4~5

Pinning information.....6

Marking.....6

Suggested solder pad layout.....6

Packing Information.....7

Reel Packing.....8

Suggested thermal profiles for soldering processes.....8

FMOSAC3080

110A 30V N-Channel Enhancement Mode Power MOSFET

Features

- $V_{DS}=30V$, $I_D=110A$.
- $R_{DS(ON)} \leq 2m\Omega$, @ $V_{GS}=10V$, $I_D=15A$.
- $R_{DS(ON)} \leq 3.5m\Omega$, @ $V_{GS}=4.5V$, $I_D=15A$.
- 100% UIS tested.
- High current capability.
- Advanced trench technology.
- Lead-free parts meet RoHS requirements.
- Suffix "-H" indicates Halogen-free part, ex.FMOSAC3080-H.

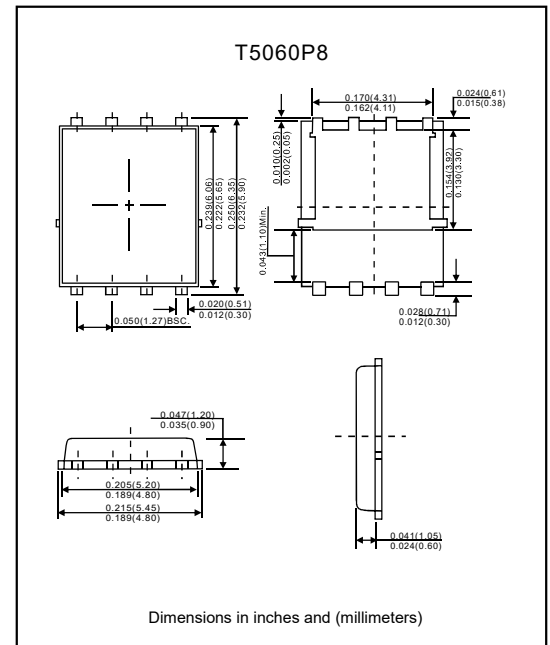
Applications

- Power management in desktop computer.
- DC/DC converters.

Mechanical data

- Epoxy:UL94-V0 rated flame retardant.
- Case : Molded plastic,T5060P8.
- Mounting Position : Any.

Package outline



Maximum ratings (At $T_c=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-source voltage	V_{DS}	30	V
Gate-source voltage	V_{GS}	± 20	V
Continuous drain current, $V_{GS}@10V$ (Note1,6) ($T_c=25^\circ C$)	I_D	110	A
($T_c=100^\circ C$)		71	
Continuous drain current, $V_{GS}@10V$ (Note1) ($T_A=25^\circ C$)		24.6	
($T_A=70^\circ C$)		19.7	
Pulsed drain current (Note2)	I_{DM}	400	A
Avalanche current	I_{AS}	64	A
Single pulse avalanche energy (Note3)	E_{AS}	204	mJ
Power dissipation (Note4) $T_c=25^\circ C$	P_D	52	W
$T_A=25^\circ C$		2.5	
Thermal resistance Junction to ambient (Note1)	$R_{\theta JA}$	50	$^\circ C/W$
Thermal resistance Junction to case (Note1)	$R_{\theta JC}$	2.4	$^\circ C/W$
Operating Junction temperature	T_J	+150	$^\circ C$
Storage temperature range	T_{STG}	-55 to +150	$^\circ C$

FMOSAC3080

Electrical characteristics (At $T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Off characteristics						
Drain-source breakdown voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	30			V
Drain-source leakage current	I _{DSS}	V _{DS} =24V, V _{GS} =0V, T _J =25°C			1	μA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C			5	
Gate-source leakage current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
On characteristics						
Gate threshold voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250μA	1.2	1.8	2.2	V
Static drain-source on-resistance (Note 2)	R _{DS(ON)}	V _{GS} =10V, I _D =15A		1.6	2	mΩ
		V _{GS} =4.5V, I _D =15A		2.7	3.5	
Dynamic parameters						
Input capacitance	C _{iss}	V _{GS} =0V, V _{DS} =15V, f=1.0MHz		2235		pF
Out capacitance	C _{oss}			956		
Reverse transfer capacitance	C _{rss}			156		
Gate resistance	R _g	V _{DS} =0V, V _{GS} =0V, f=1.0MHz		1.6		Ω
Switching parameters						
Total gate charge	Q _g	V _{GS} =4.5V, V _{DS} =15V, I _D =15A		20.4		nC
Gate to source charge	Q _{gs}			6.7		
Gate to drain charge	Q _{gd}			8.6		
Turn-on delay time	t _{d(on)}	V _{DD} =15V, V _{GS} =10V, R _e =3.3Ω, I _D =1A		12		ns
Rise time	t _r			25		
Turn-off delay time	t _{d(off)}			39		
Fall time	t _f			23		
Source-drain diode ratings and characteristics						
Drain-source diode forward voltage (Note 2)	V _{SD}	I _S =1A, V _{GS} =0V, T _J =25°C			1.2	V
Continuous source current (Note 1, 6)	I _S	V _G =V _D =0V, Force current			100	A
Reverse recovery time	t _{rr}	I _F =15A , dI/dt=100A/μA, T _J =25°C		62.2		nS
Reverse recovery charge	Q _{rr}			46.4		nC

Note: 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

3. The E_{AS} data shows Maximum rating. The test condition is $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=52\text{A}$.

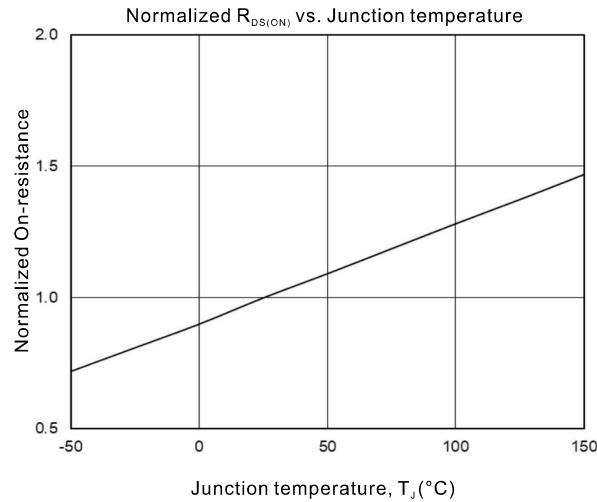
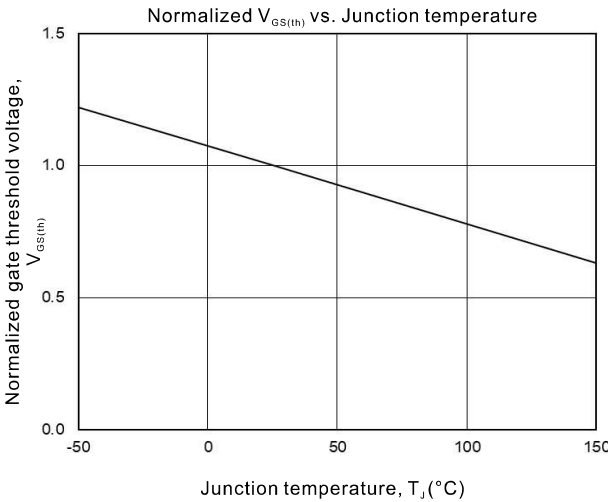
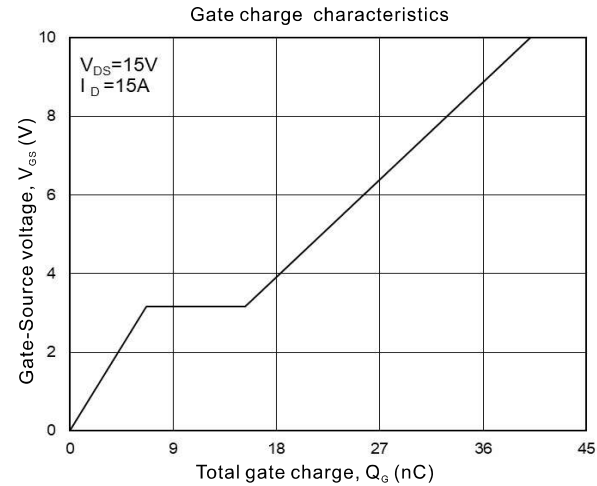
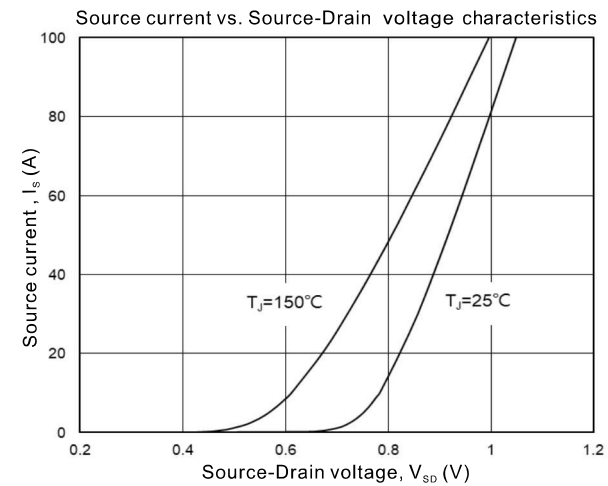
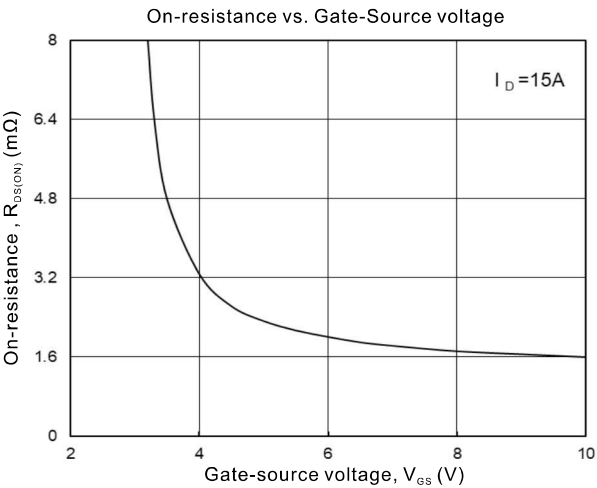
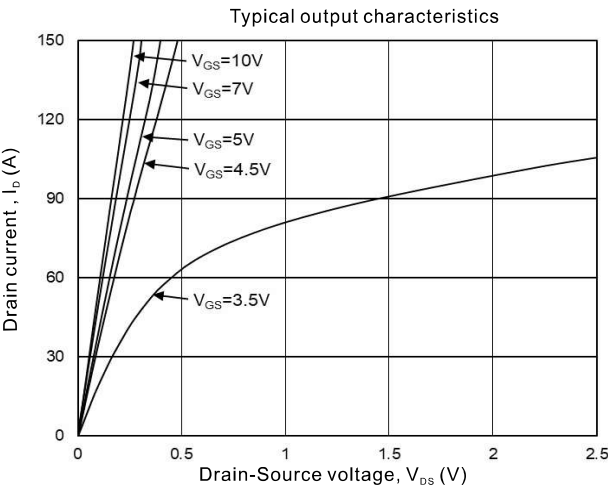
4. The power dissipation is limited by 150°C junction temperature.

5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

6. Package limitation current is 100A.

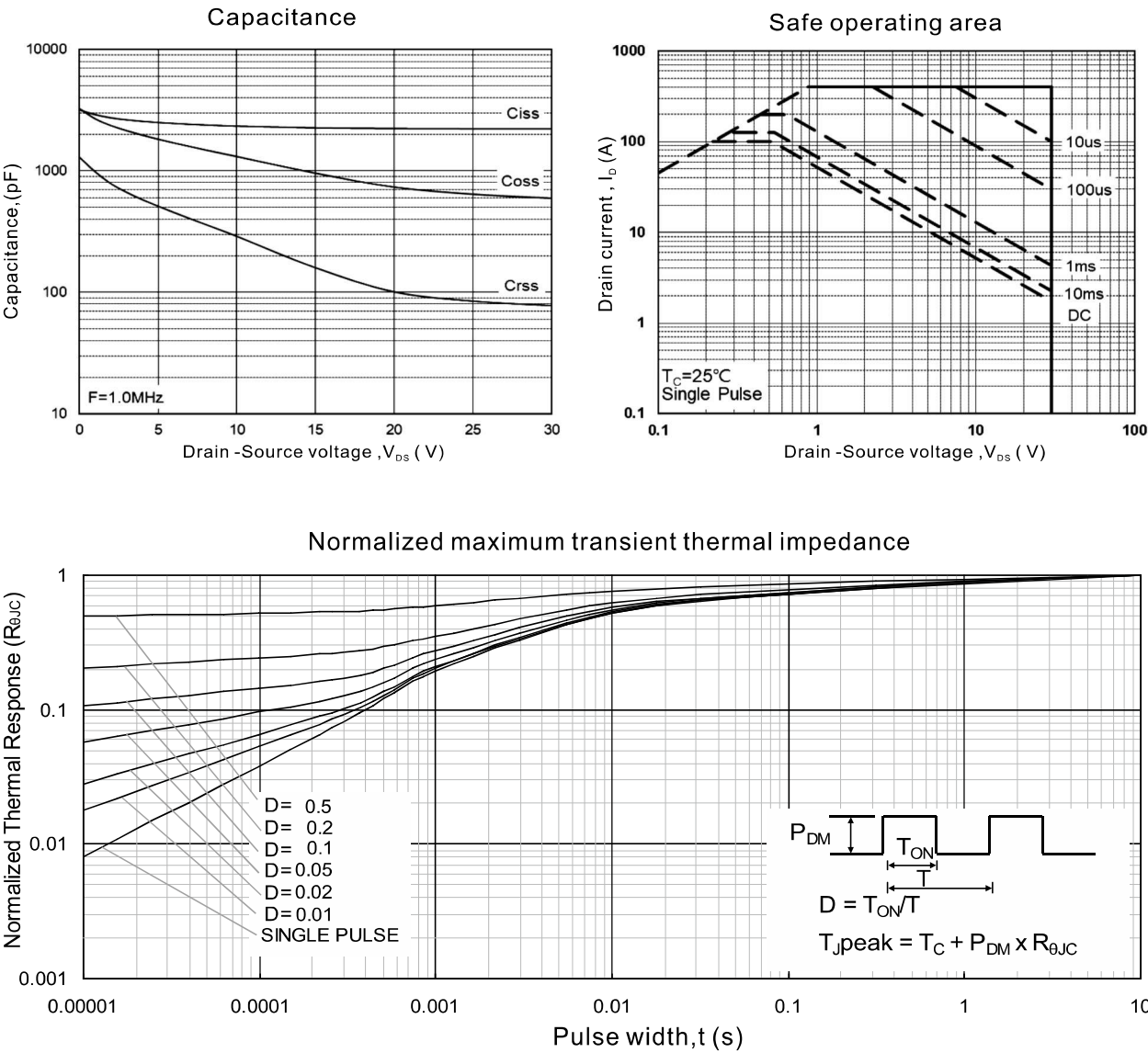
FMOSAC3080

Rating and characteristic curves



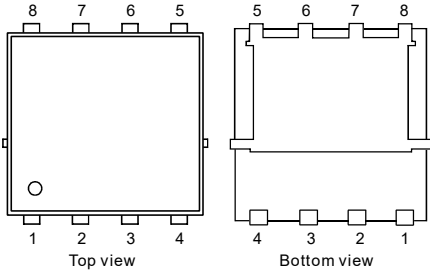
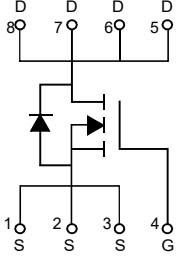
FMOSAC3080

Rating and characteristic curves



FMOSAC3080

Pinning information

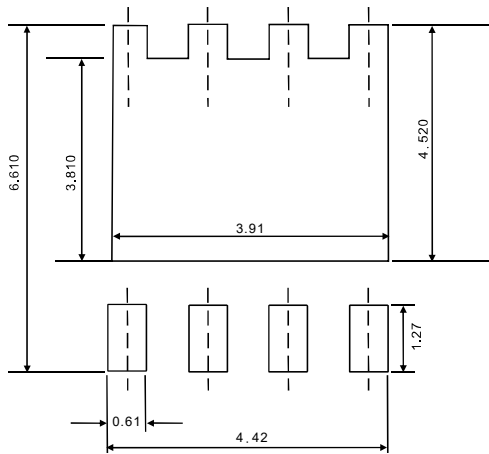
Pin	Simplified outline	Symbol
Pin 1, 2, 3, Source Pin 4 Gate Pin 5, 6, 7, 8 Drain	 Top view Bottom view	

Marking

Type number	Marking code
FMOSAC3080	AC3080

Suggested solder pad layout

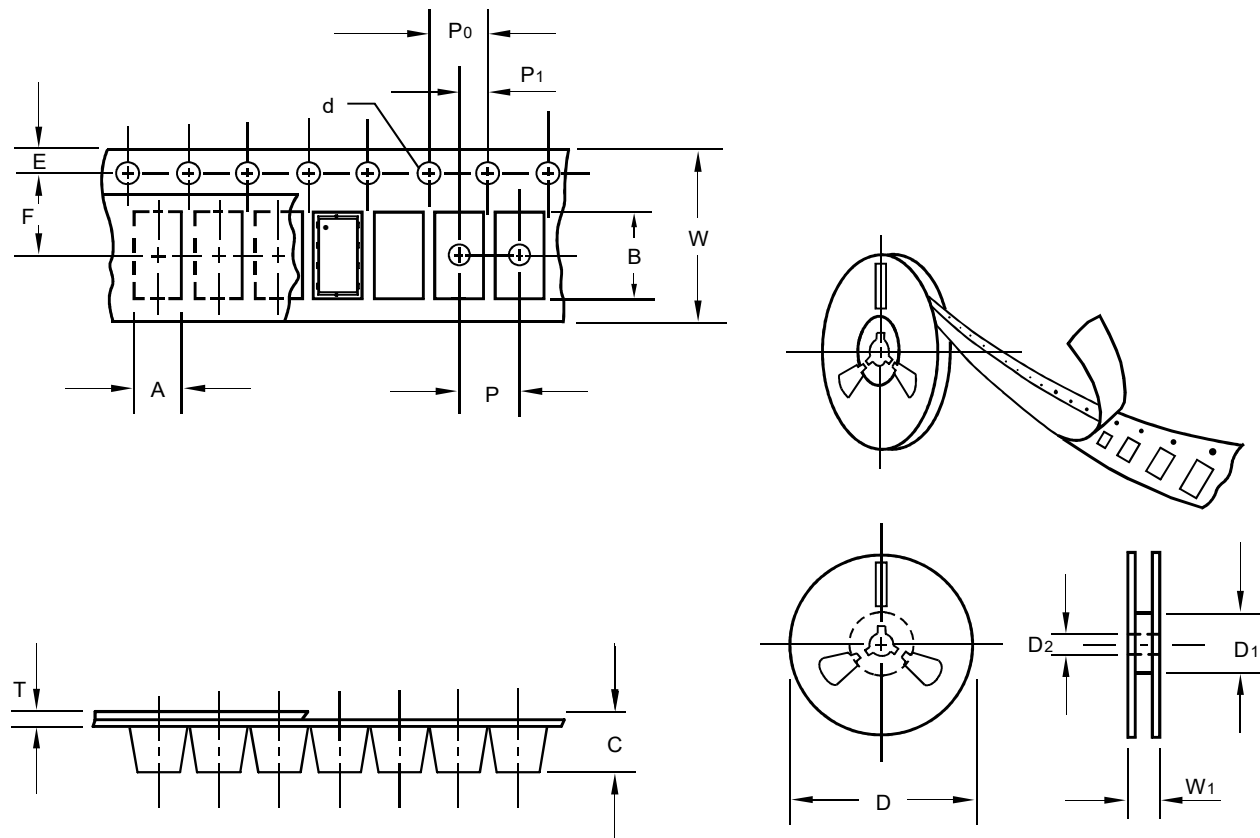
T5060P8



Note:
1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.1 mm
3. The pad layout is for reference purposes only.

FMOSAC3080

Packing information



unit:mm

Item	Symbol	Tolerance	T5060P8
Carrier width	A	0.1	6.50
Carrier length	B	0.1	5.30
Carrier depth	C	0.1	1.40
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	330.00
13" Reel inner diameter	D1	min	176.00
7" Reel outside diameter	D	2.0	-
7" Reel inner diameter	D1	min	-
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	5.50
Punch hole pitch	P	0.1	8.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.30
Tape width	W	0.3	12.00
Reel width	W1	1.0	18.40

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

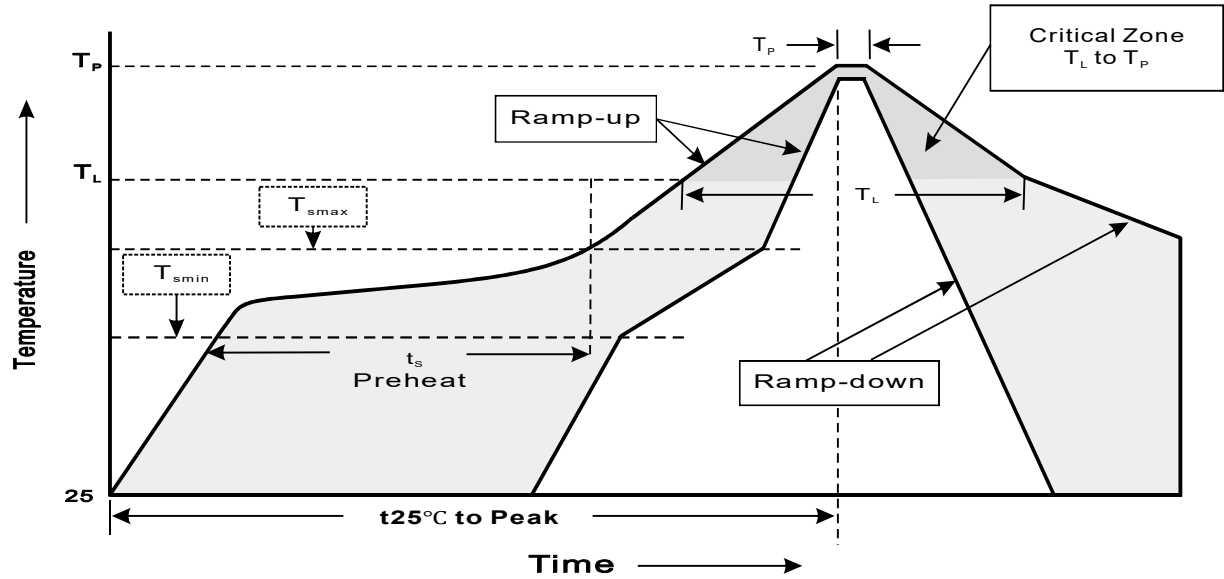
FMOSAC3080

Reel packing

Device	Outline	Device Package	Reel Size	Reel (pcs)
FMOSAC3080	Taping	T5060P8	13 inch	3,000

Suggested thermal profiles for soldering processes

- 1.Storage environment : Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile feature	Soldering Condition
Average ramp-up rate (T_L to T_p)	< 3°C/sec
Preheat -Temperature Min (T_{smin}) -Temperature Max (T_{smax}) -Time (min to max) (t_s)	150°C 200°C 60 ~ 120sec
T_{smax} to T_L -Ramp-up rate	< 3°C/sec
Time maintained above: -Temperature (T_L) -Time(T_L)	217°C 60 ~ 260 sec
Peak Temperature(T_p)	255°C -0/+5°C
Time within 5°C of actual peak Temperature(T_p)	10 ~ 30sec
Ramp-down rate	< 6°C/sec
Time 25°C to peak temperature	< 6 minutes