

LL24BT1006B

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32W Surface Mount TVS Bi-Directional For ESD Protection : 24V

Features

- Solid-state silicon-avalanche technology.
- Protection one data/power line to:
IEC 61000-4-2 $\pm 8\text{kV}$ (Contact), $\pm 15\text{kV}$ (Air).
IEC 61000-4-4 (EFT) 40A (5/50ns).
IEC 61000-4-5 (Lightning) 4A (8/20 μs).
- Lead-free parts meet RoHS requirements.
- Suffix "-H" indicates Halogen-free parts, ex. LL24BT1006B-H.

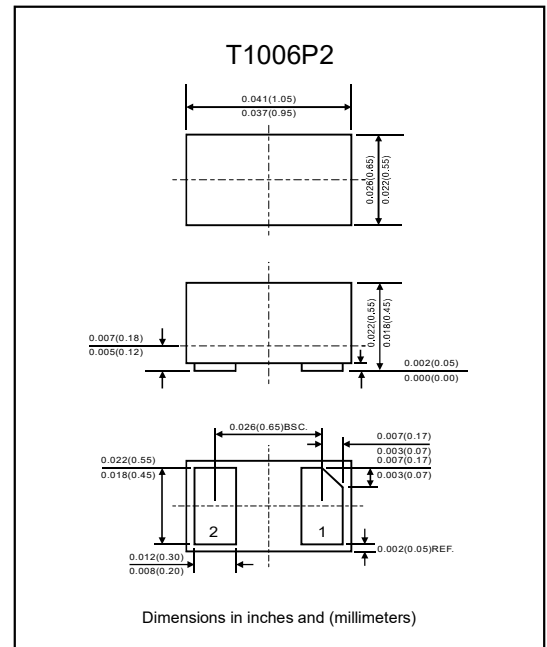
Applications

- Microprocessor based equipment.
- Notebooks, desktops and servers.
- Portable instrumentation.

Mechanical data

- Epoxy: UL94-V0 rated flame retardant.
- Case: Molded plastic, DFN1006-2 (T1006P2) package.
- Terminals: High temperature soldering guaranteed 260°C/10sec.

Package outline



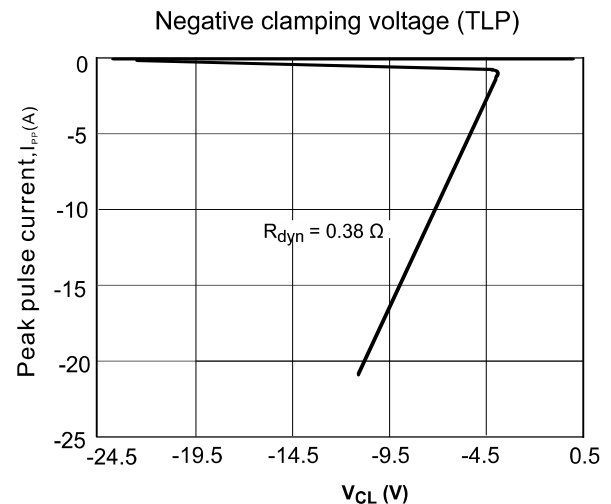
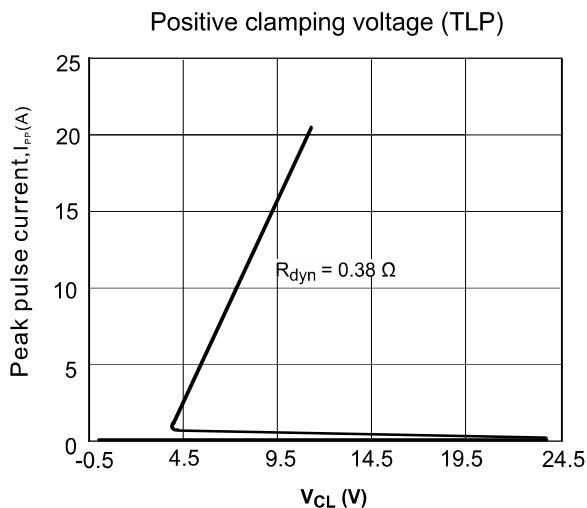
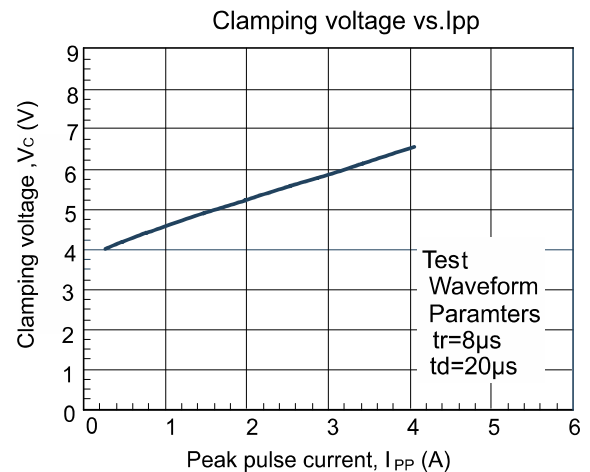
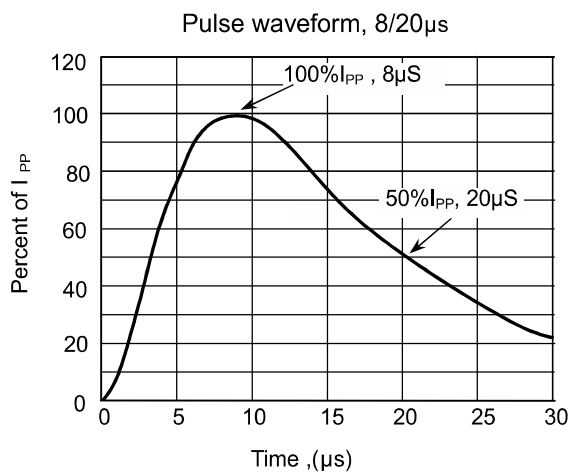
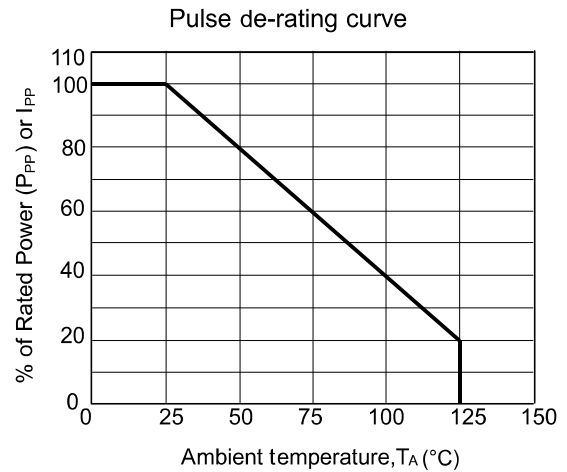
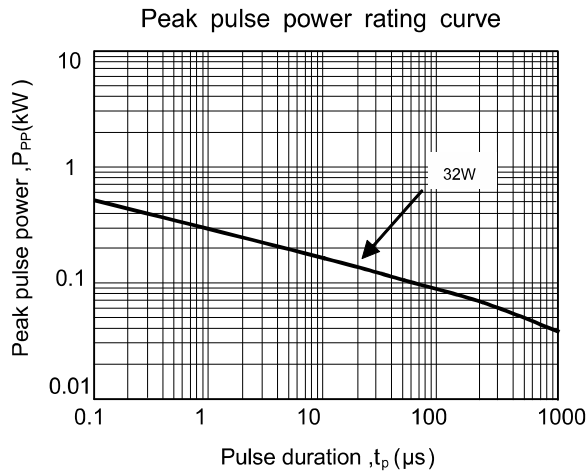
Maximum ratings (at $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak pulse power ($t_p = 8/20\mu\text{s}$)	P_{PP}	32	W
ESD Per IEC 61000-4-2 (Air)	V_{ESD}	± 15	KV
ESD Per IEC 61000-4-2 (Contact)		± 8	
Peak pulse current ($t_p = 8/20\mu\text{s}$)	I_{PP}	4	A
Operating temperature	T_J	+125	$^\circ\text{C}$
Storage temperature	T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical characteristics (at $T_A = 25^\circ\text{C}$ unless otherwise noted)

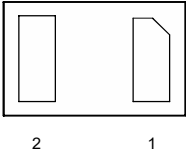
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Reverse working voltage	V_{RWM}				24	V
Holding voltage	V_H	$I_T = I_H$		3		V
Holding current	I_H		35			mA
Reverse leakage current	I_R	$V_{RWM} = 22\text{V}$			0.5	μA
Clamping voltage 1	V_{C1}	$I_{PP1} = 4\text{A}$ ($t_p = 8/20\mu\text{s}$)		8		V
Clamping voltage 2 (TLP)	V_{C2}	$I_{PP2} = 16\text{A}$ ($t_p = 0.1\mu\text{s}$)		9.6		V
Trigger voltage	V_T			24		V
Dynamic resistance	R_{dyn}	$t_p = 100\text{ns}$		0.38		Ω
Junction capacitance	C_J	$V_R = 0\text{V}$, $f = 1.0\text{MHz}$		0.45		pF

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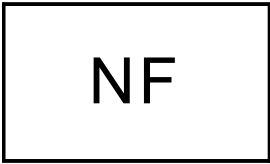
Typical performance characteristics ($T_A=25^\circ\text{C}$ Unless otherwise specified)

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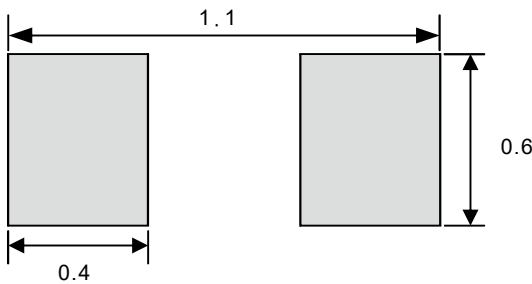
Pinning information

Pin	Simplified outline	Symbol
Bi-Directional		

Marking

Type number	Marking code
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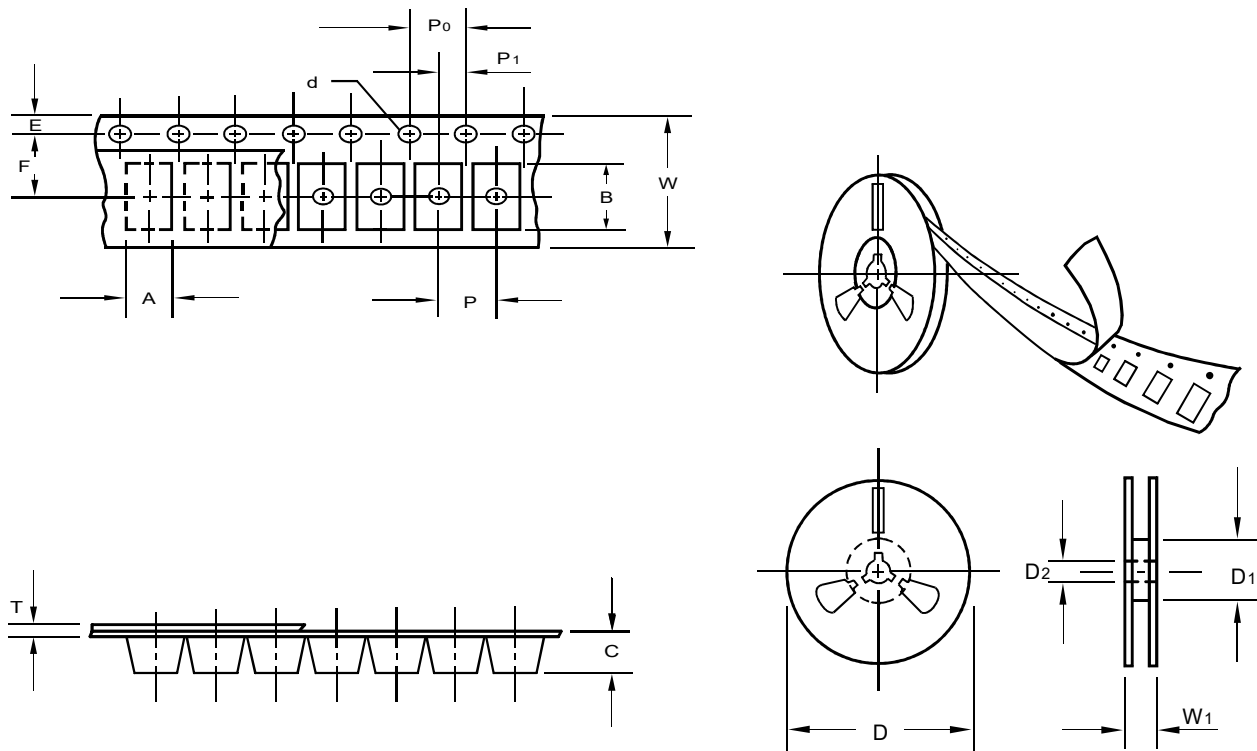
Suggested solder pad layout



Unit: mm

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Suggested solder pad layout



unit:mm

Item	Symbol	Tolerance	T1006P2
Carrier width	A	0.1	0.75
Carrier length	B	0.1	1.15
Carrier depth	C	0.1	0.60
Sprocket hole	d	0.05	1.55
7" Reel outside diameter	D	1.0	178.00
7" Reel inner diameter	D ₁	Min.	60.0
Feed hole diameter	D ₂	0.2	13.0
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P ₀	0.1	4.00
Embossment center	P ₁	0.05	2.00
Overall tape thickness	T	0.05	0.23
Tape width	W	0.2	8.00
Reel width	W ₁	Max.	13.5

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

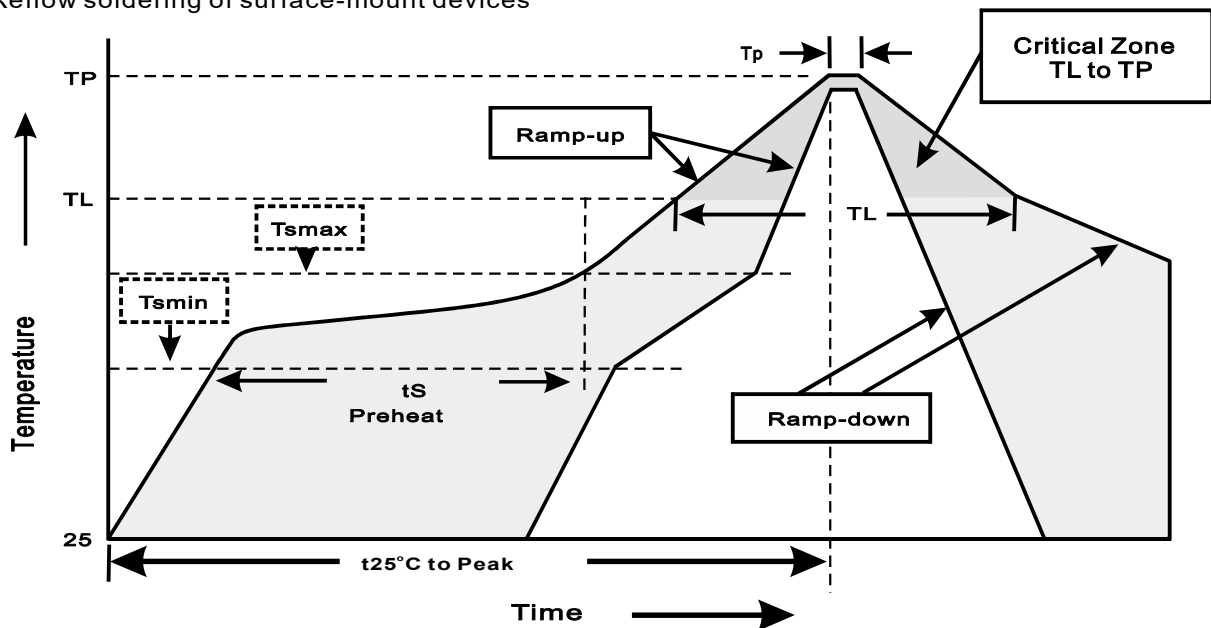
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Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)
T1006P2	7"	10,000	4.0	100,000	183*123*183	178	382*257*387	800,000

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



- 3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat - Temperature Min(T _{smin}) - Temperature Max(T _{smax}) - Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smax} to T _L - Ramp-upRate	<3°C/sec
Time maintained above: - Temperature(T _L) - Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _p)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes