

L08BT1006

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Surface Mount TVS Bi-Directional For ESD Protection : 8V

Features

- Protects one data or power line.
- Operating voltage : 8V
- Low clamping voltage
- IEC 61000-4-2 (ESD) immunity test : Air discharge: $\pm 30\text{kV}$
Contact discharge: $\pm 30\text{kV}$
- IEC61000-4-5 (Lightning) 6A (8/20 μs)
- We declare that the material of product compliance with RoHS requirements.
- Suffix "-H" indicates Halogen-free parts, ex.L08BT1006-H.

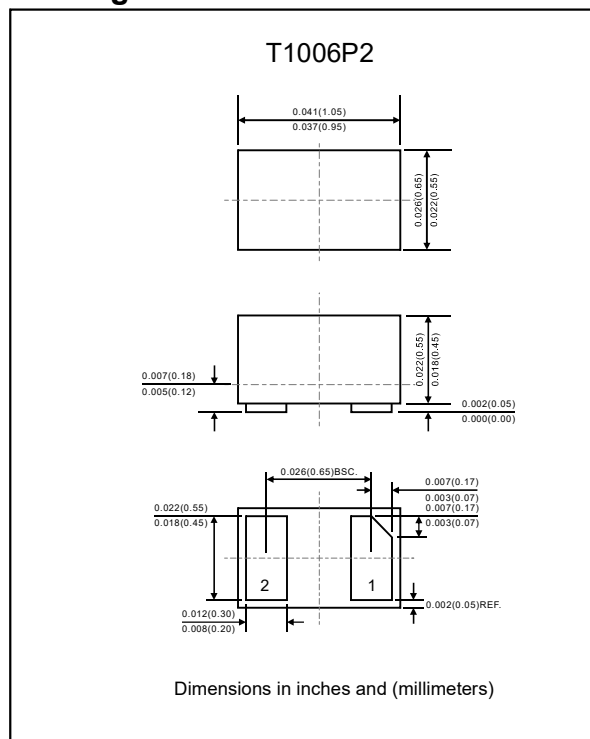
Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- Digital Cameras
- Peripherals
- Audio Players
- Keypads, Side Keys, LCD Displays

Mechanical data

- T1006P2 package
- Molding compound flammability rating : UL94V-0
- Making part number, data code.
- Packaging : Tape and reel.
- Weight : Approximated 0.73 mg.

Package outline



Maximum ratings (at $T_A = 25^\circ\text{C}$ unless otherwise noted)

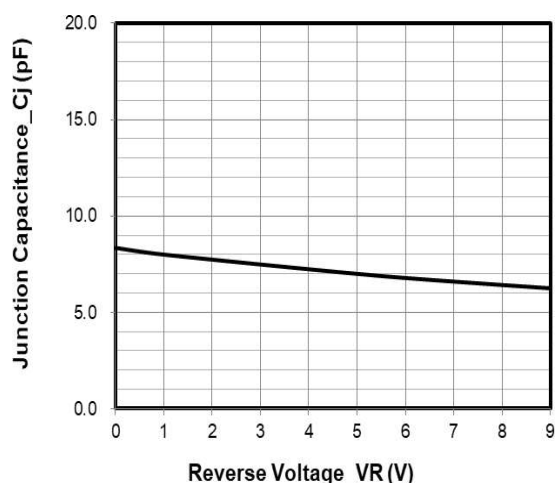
Parameter	Symbol	Value	Unit
Peak pulse power (8/20 μs)	P_{PK}	100	W
Peak pulse current (8/20 μs)	I_{PP}	6	A
ESD Per IEC 61000-4-2 (Air)	V_{ESD}	± 30	KV
ESD Per IEC 61000-4-2 (Contact)		± 30	
Operating temperature range	T_J	$-55 \sim +125$	$^\circ\text{C}$
Storage temperature	T_{STG}	-55 to $+150$	$^\circ\text{C}$
Maximum lead temperature for soldering during 10s	T_L	260	$^\circ\text{C}$

Electrical characteristics (at $T_A = 25^\circ\text{C}$ unless otherwise noted)

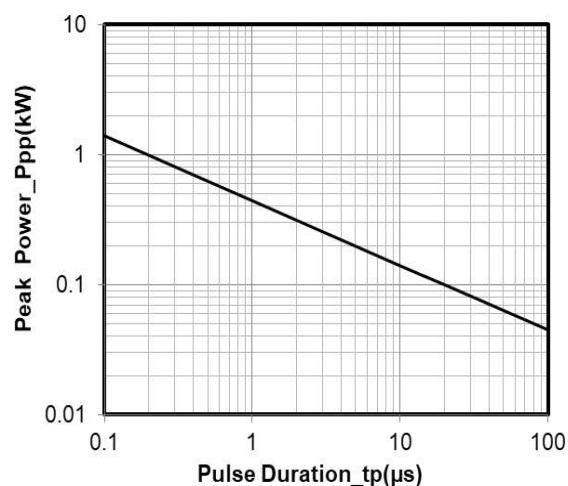
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Reverse working voltage	V_{RWM}				8	V
Breakdown voltage	V_{BR}	$I_T = 1\text{mA}$	8.5			V
Reverse leakage current	I_R	$V_{RWM} = 8\text{V}$			0.5	μA
Clamping voltage 1	V_{C1}	$I_{PP1} = 1\text{A}$ (8 x 20 μs Pulse)			11	V
Clamping voltage 2	V_{C2}	$I_{PP2} = 1\text{A}$ (8 x 20 μs Pulse)			18	V
Junction capacitance	C_P	$V_R = 0\text{V}$, $f = 1\text{MHz}$		9		pF

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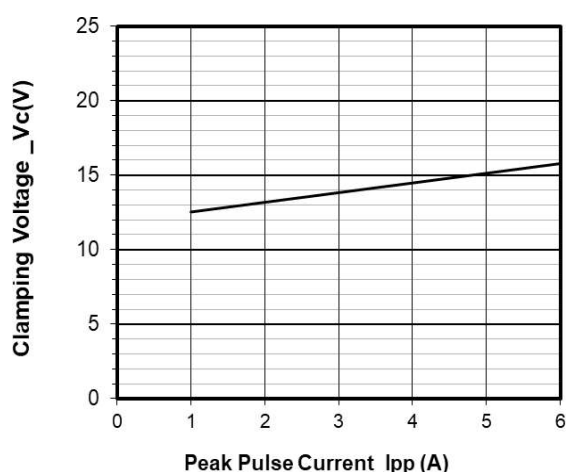
Typical performance characteristics ($T_A=25^{\circ}\text{C}$ Unless otherwise specified)



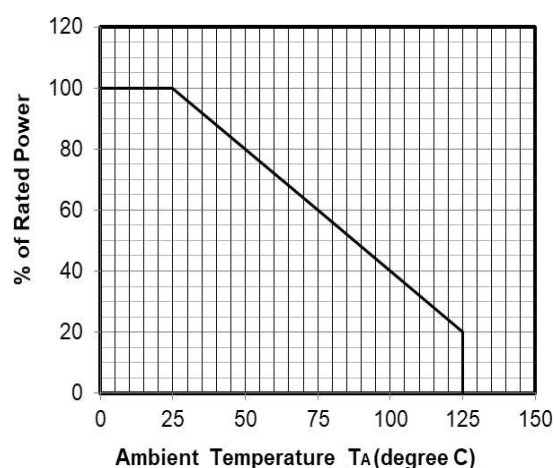
Junction Capacitance vs. Reverse Voltage



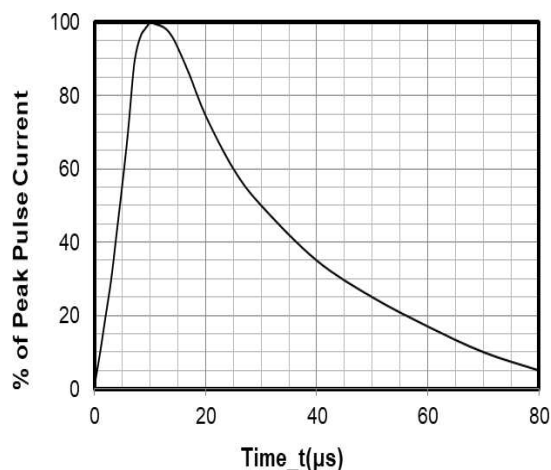
Peak Pulse Power vs. Pulse Time



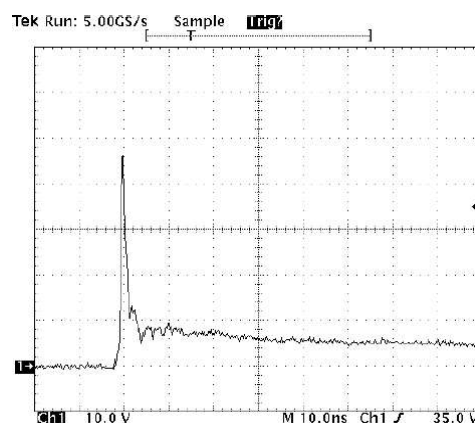
Clamping Voltage vs. Peak Pulse Current ($t_p = 8/20\mu\text{s}$)



Power Derating Curve



8 X 20 μs Pulse Waveform

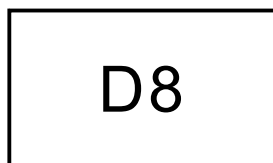


Note: Data is taken with a 10x attenuator

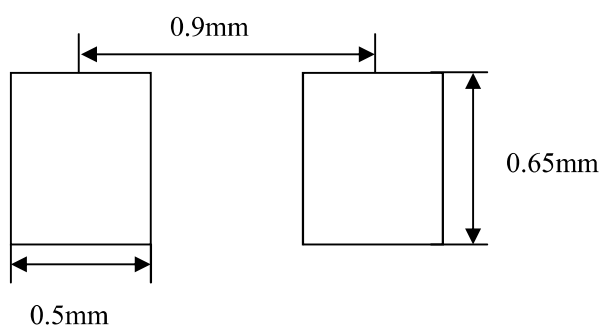
8 kV Contact per IEC61000-4-2

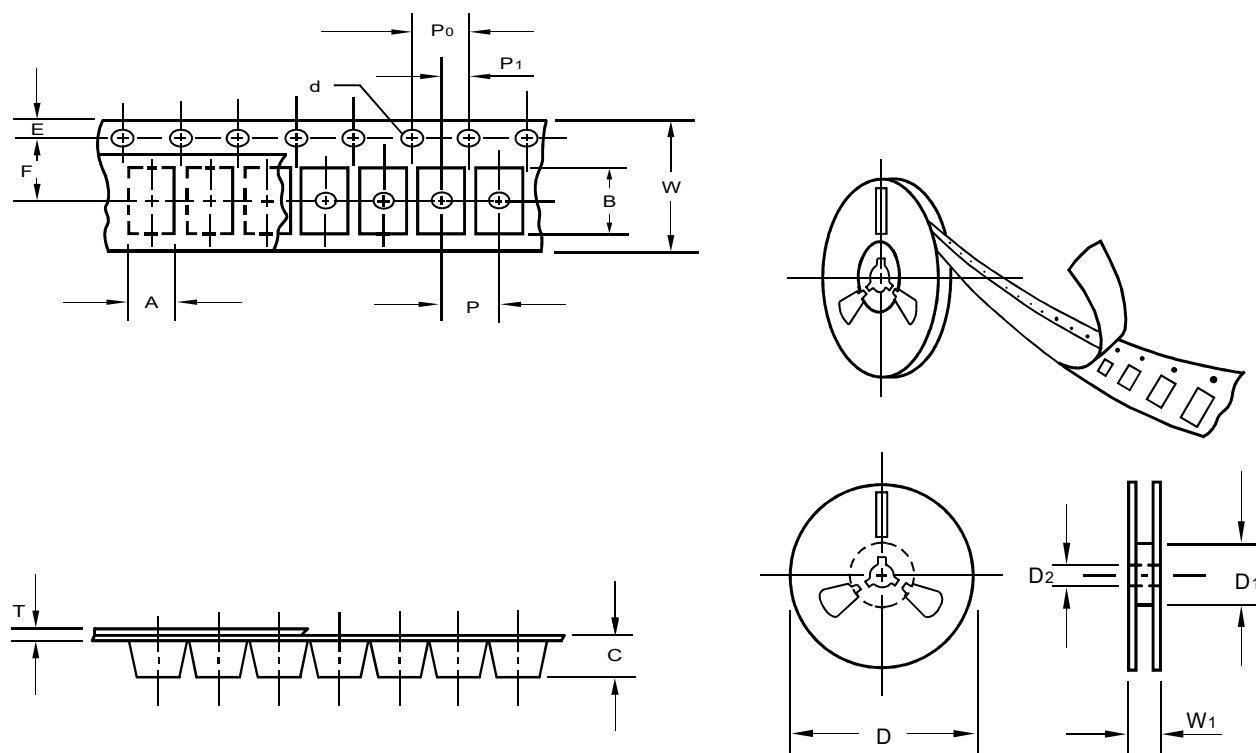
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Marking Information



Suggested solder pad layout



L08BT1006**Suggested solder pad layout**

unit:mm

Item	Symbol	Tolerance	T1006P2
Carrier width	A	0.1	0.75
Carrier length	B	0.1	1.15
Carrier depth	C	0.1	0.60
Sprocket hole	d	0.05	1.55
7" Reel outside diameter	D	1.0	178.00
7" Reel inner diameter	D1	Min.	60.0
Feed hole diameter	D2	0.2	13.0
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.05	2.00
Overall tape thickness	T	0.05	0.23
Tape width	W	0.2	8.00
Reel width	W1	Max.	13.5

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

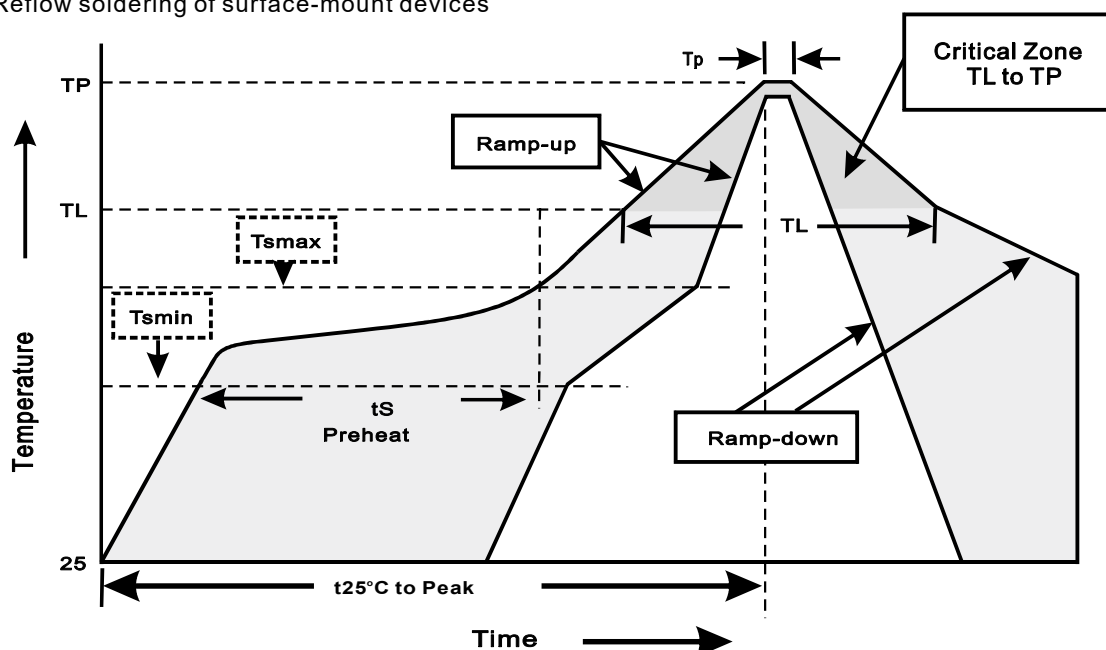
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Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
T1006P2	7"	10,000	4.0	100,000	183*123*183	178	382*257*387	800,000	15.0

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smmin}) -Temperature Max(T _{smmax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smmax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes