

FMUN2112

List

List.....	1
Package outline.....	2
Features.....	2
Mechanical data.....	2
Maximum ratings	2
Electrical characteristics.....	2
Rating and characteristic curves.....	3
Pinning information.....	4
Marking.....	4
Suggested solder pad layout.....	4
Reel packing.....	5
Suggested thermal profiles for soldering processes.....	5

FMUN2112

PNP Silicon Epitaxial Planar Digital Transistor

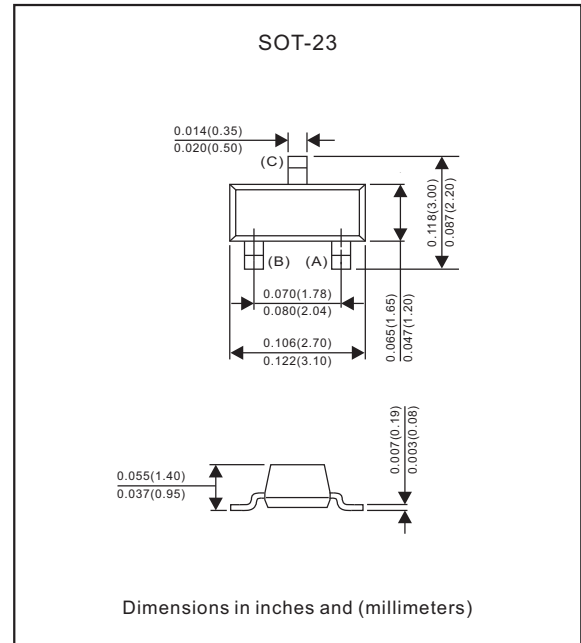
Features

- For Switching and interface circuit and drive circuit applications.
- Simplify circuit design.
- Reduce a quantity of parts and manufacturing process.
- We declare that the material of product compliance with RoHS requirements.
- Suffix "-H" indicates Halogen free parts, ex. FMUN2112-H.

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, SOT-23
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any

Package outline



Maximum ratings (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Collector to Base voltage	$-V_{CBO}$	50	V
Collector to Emitter voltage	$-V_{CEO}$	50	V
Output current	$-I_C$	100	mA
Power dissipation	P_{tot}	200	mW
Junction temperature	T_J	150	$^{\circ}\text{C}$
Storage temperature range	T_{stg}	-55 to +150	$^{\circ}\text{C}$

Electrical characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

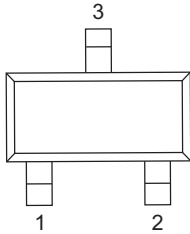
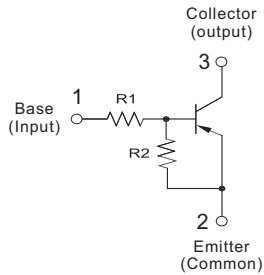
Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
DC current gain	$-V_{CE}=10\text{V}, -I_C=5\text{mA}$	h_{FE}	60			
Collector Base cut-off current	$-V_{CB}=50\text{V}$	$-I_{CBO}$			100	nA
Collector Emitter cut-off current	$-V_{CE}=50\text{V}$	$-I_{CEO}$			500	nA
Collector Emitter saturation voltage	$-V_{EB}=6\text{V}$	$-I_{EBO}$			0.2	mA
Collector Base breakdown voltage	$-I_C=10\mu\text{A}$	$-V_{(BR)CBO}$	50			V
Collector Emitter breakdown voltage	$-I_C=2\text{mA}$	$-V_{(BR)CEO}$	50			V
Collector Emitter saturation voltage	$-I_C=10\text{mA}, -I_B=0.3\text{mA}$	$-V_{CE(sat)}$			0.25	V
Output voltage (on)	$-V_{CC}=5\text{V}, -V_B=2.5\text{V}, R_L=1\text{K}\Omega$	$-V_{OL}$			0.2	V
Output voltage (off)	$-V_{CC}=5\text{V}, -V_B=0.5\text{V}, R_L=1\text{K}\Omega$	$-V_{OH}$	4.9			V
Input resistance		R_i	15.4	22	28.6	K Ω
Resistance ratio		R_1 / R_2	0.8	1.0	1.2	

Note :

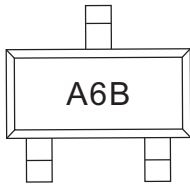
- 1.Characteristic of transistor only.

FMUN2112

Pinning information

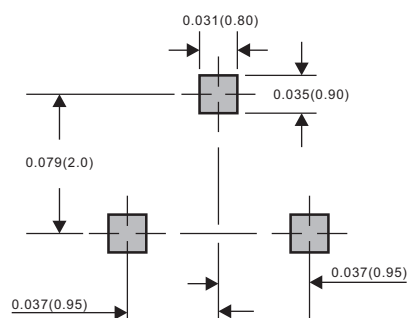
Pin	Simplified outline	Symbol
Pin1 Base Pin2 Emitter Pin3 Collector		

Marking

Type numbe	Marking code
FMUN2112	

Suggested solder pad layout

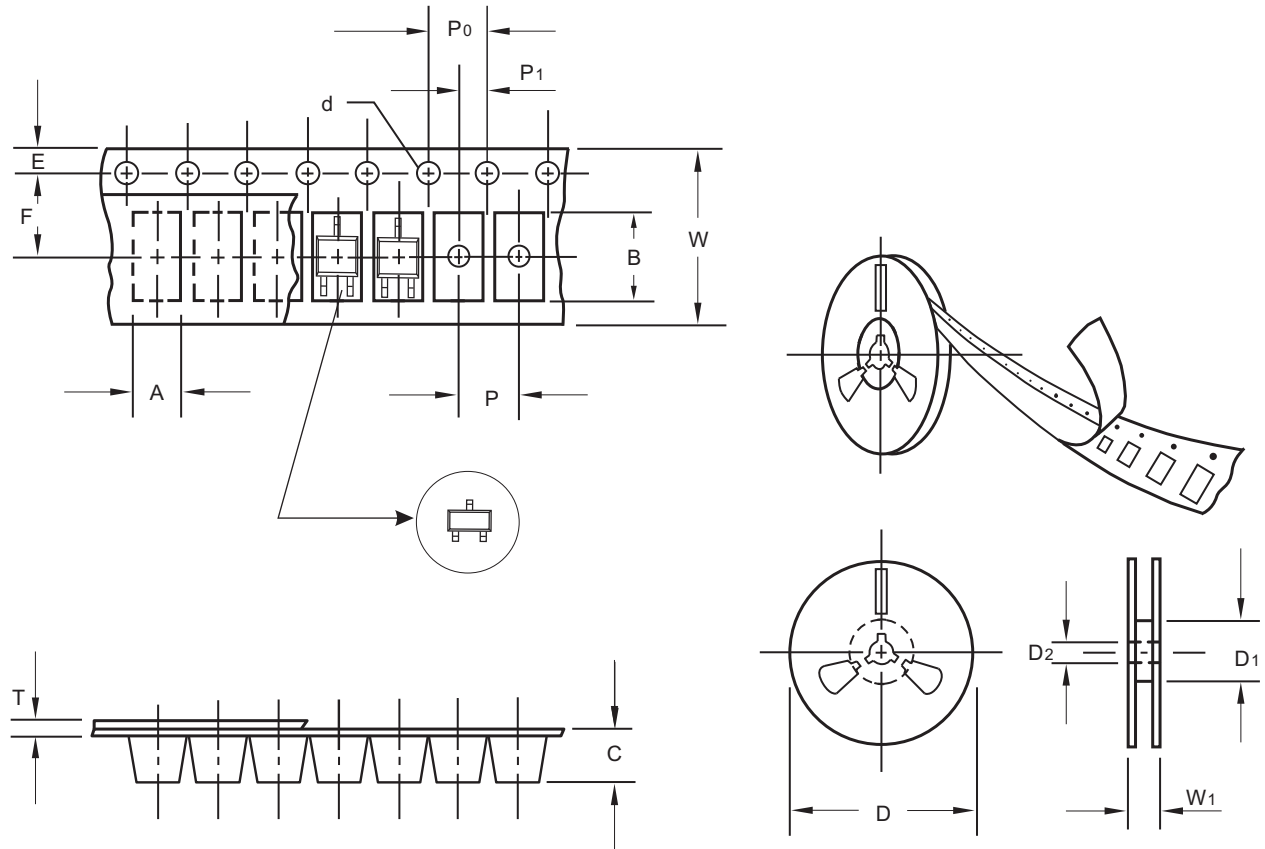
SOT-23



Dimensions in inches and (millimeters)

FMUN2112

Packing information



unit:mm

Item	Symbol	Tolerance	SOT-23
Carrier width	A	0.1	3.15
Carrier length	B	0.1	2.77
Carrier depth	C	0.1	1.22
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	-
13" Reel inner diameter	D1	min	-
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	55.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	12.0

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

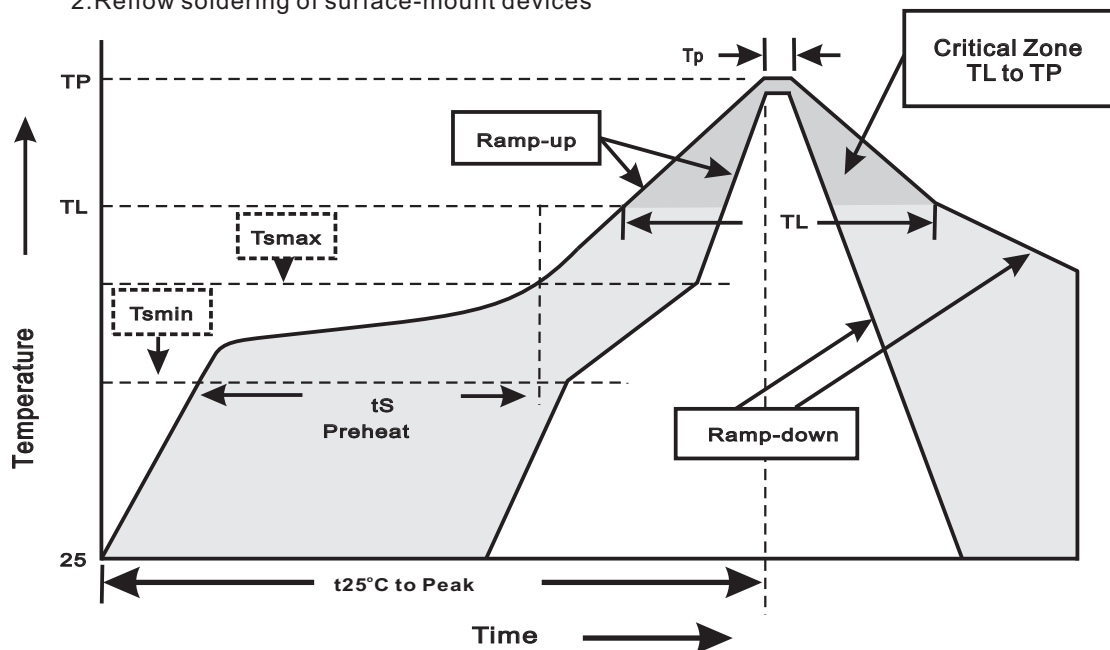
FMUN2112

Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOT-23	7"	3,000	4.0	30,000	183*123*183	178	382*257*387	240,000	11.6

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smín}) -Temperature Max(T _{smáx}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smáx} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _p)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes