

DTC144EE

List

List.....	1
Package outline.....	2
Features.....	2
Applications.....	2
Mechanical data.....	2
Maximum ratings	2
Electrical characteristics.....	2
Rating and characteristic curves.....	3
Pinning information.....	4
Marking.....	4
Suggested solder pad layout.....	4
Suggested thermal profiles for soldering processes.....	5

DTC144EE

NPN Silicon Epitaxial Planar Digital Transistor

Features

- Built-in bias resistors enable the configuration of an inverter circuit Without connecting external input resistors(see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, marking device design easy.
- Reduce a quantity of parts and manufacturing process.
- We declare that the material of product compliance with RoHS requirements.
- Suffix "-H" indicates Halogen free parts, ex. DTC144EE-H.

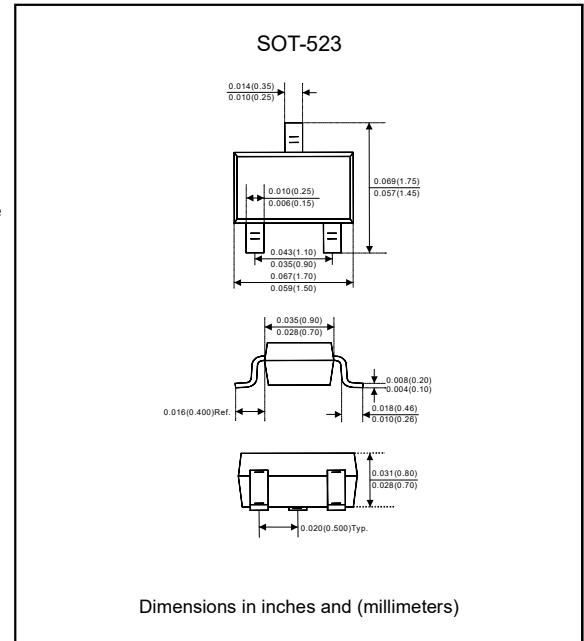
Applications

- Inverter, Interface, Driver

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, SOT-523
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any

Package outline



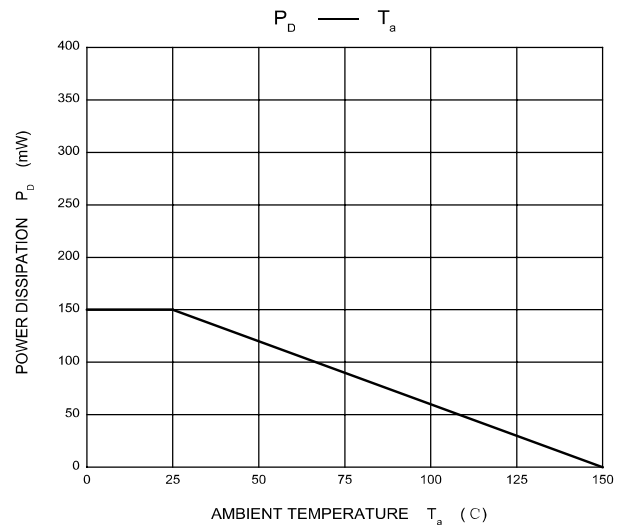
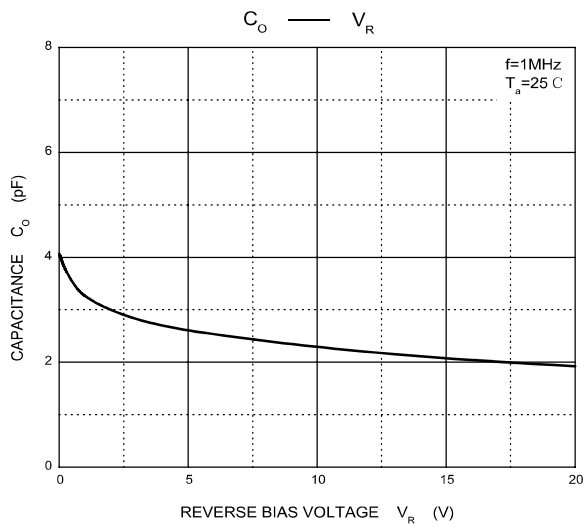
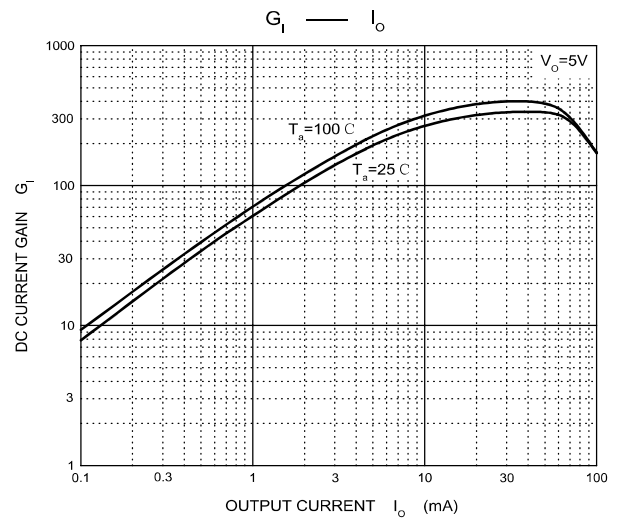
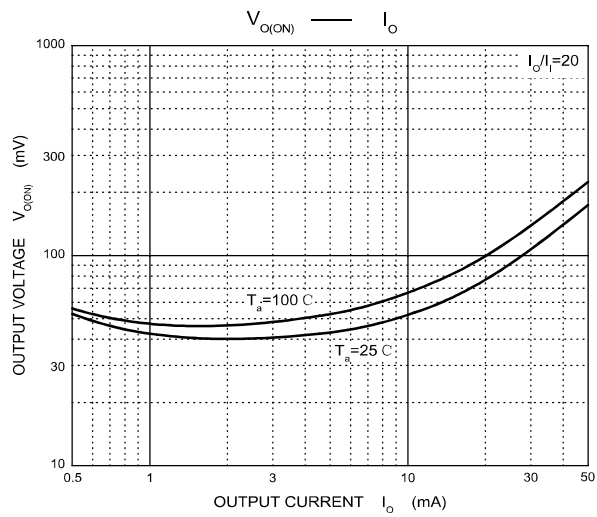
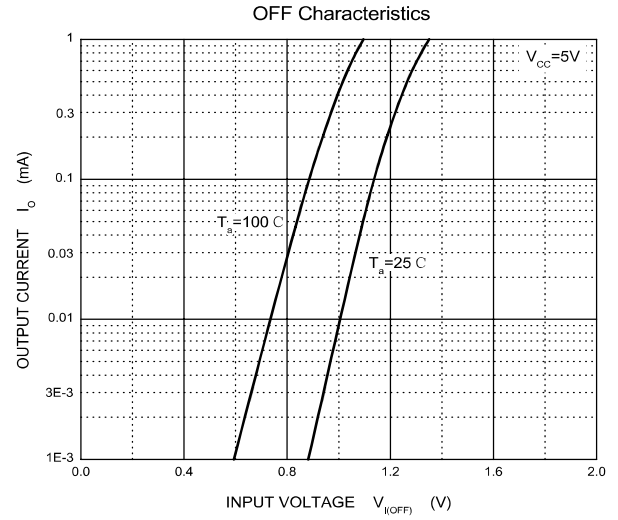
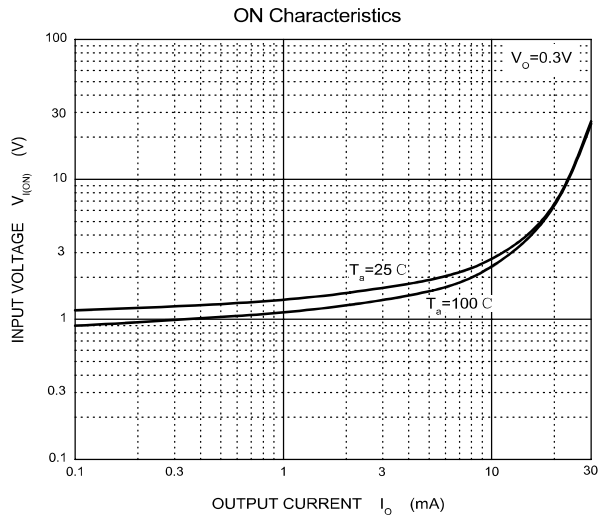
Maximum ratings (AT $T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Collector to emitter voltage	V_{CE0}	50	V
Input voltage	V_i	-10 to +40	V
Collector current	I_C	30	mA
Peak collector current	I_{CM}	100	mA
Power dissipation	P_{tot}	150	mW
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55 to +150	$^\circ\text{C}$

Electrical characteristics (AT $T_a=25^\circ\text{C}$ unless otherwise noted)

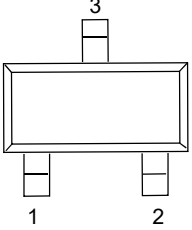
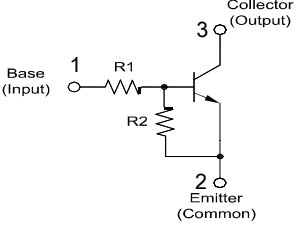
Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
Input voltage	$V_{CE}=5V, I_C=100\mu A$	$V_{I(off)}$	0.5			V
	$V_o=0.3V, I_o=2mA$	$V_{I(on)}$			3	V
Output voltage	$I_o/I_i=10mA/0.5mA$	$V_{O(on)}$			0.3	V
Input current	$V_i=5V$	I_i			0.18	mA
Output current	$V_{CC}=50V, V_i=0V$	$I_{O(off)}$			0.5	μA
DC current gain	$V_o=5V, I_o=5mA$	h_{FE}	68			
Transition frequency	$V_{CE}=10V, I_o=5mA, f=100MHZ$	f_T		250		MHZ
Input resistance		R_1	32.9	47	61.1	K Ω
Resistance ratio		R_2 / R_1	0.8	1	1.2	

Rating and characteristic curves (DTC144EE)



DTC144EE

Pinning information

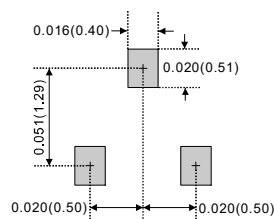
Pin	Simplified outline	Symbol
Pin1 BASE Pin2 EMITTER Pin3 COLLECTOR		

Marking and resistor values

Type number	Marking code
DTC144EE	26

Suggested solder pad layout

SOT-523

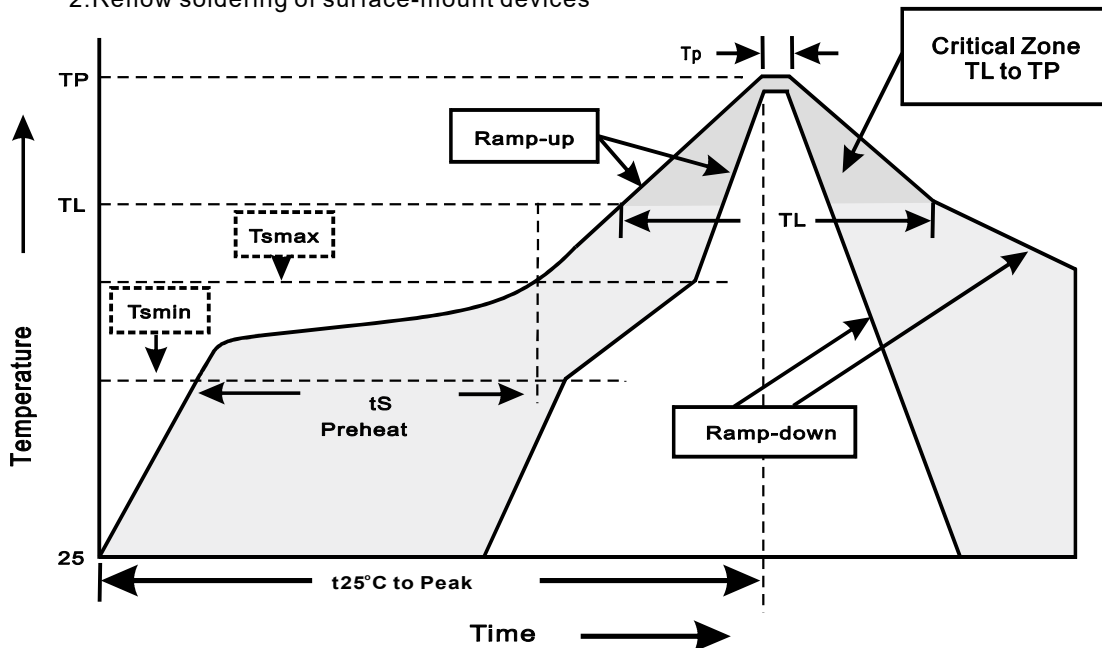


Dimensions in inches and (millimeters)

DTC144EE

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smIn}) -Temperature Max(T _{smax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _p)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes