

DTA114YE

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DTA114YE

PNP Silicon Epitaxial Planar Digital Transistor

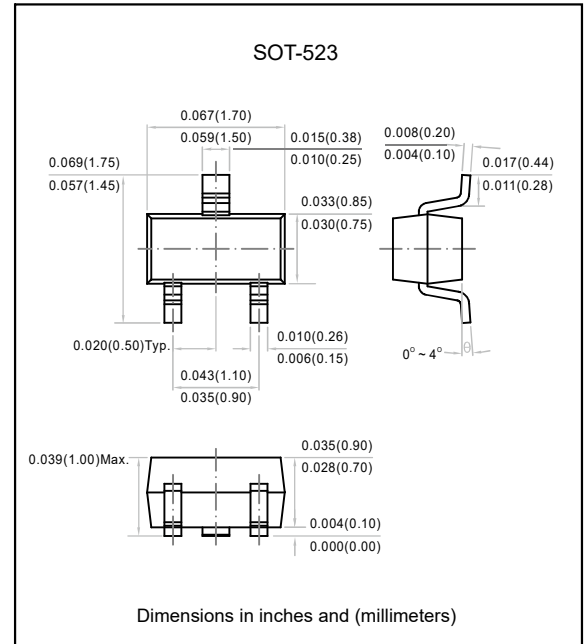
Features

- Built-in bias resistors that implies easy ON/OFF applications.
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input.
- Only the on/off conditions need to be set for operation, making device design easy.
- We declare that the material of product compliance with RoHS requirements.
- Suffix "-H" indicates Halogen free parts, ex. DTA114YE-H.

Mechanical data

- Epoxy: UL94-V0 rated flame retardant
- Case : Molded plastic, SOT-523
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any

Package outline



Maximum ratings (At $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Supply voltage	$-V_{CC}$	50	V
Input voltage	V_{IN}	-40 to +6	V
Output current	$-I_{OUT}$	70	mA
Peak collector current	$-I_{C(MAX)}$	100	mA
Power dissipation	P_D	390	mW
Junction temperature	T_J	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55 to +150	$^\circ\text{C}$
Junction to ambient	$R_{\theta JA}$	318	$^\circ\text{C/W}$
Junction to case	$R_{\theta JC}$	145	$^\circ\text{C/W}$

Electrical characteristics (At $T_A=25^\circ\text{C}$ unless otherwise noted)

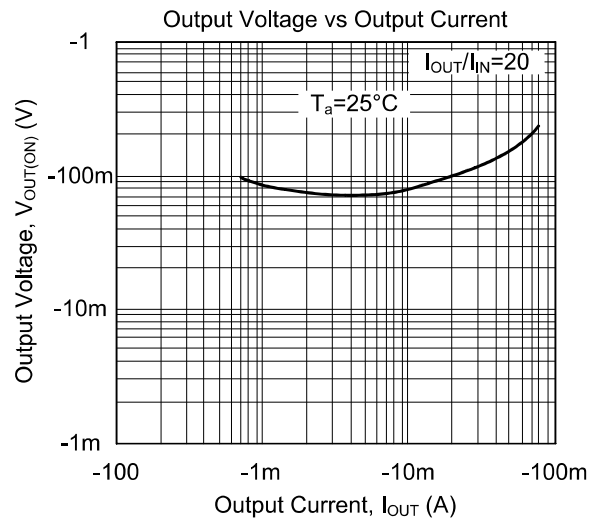
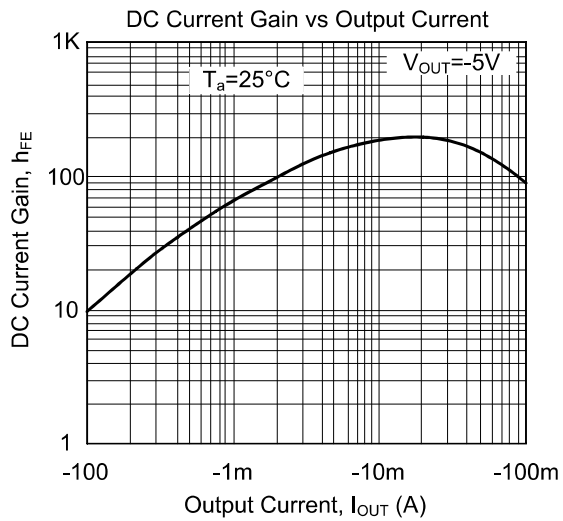
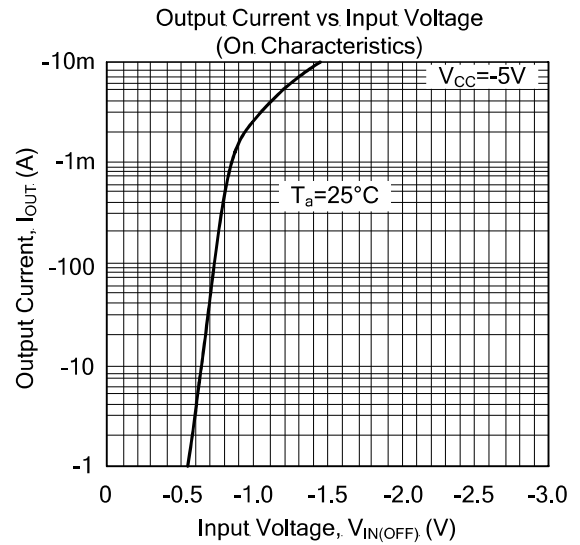
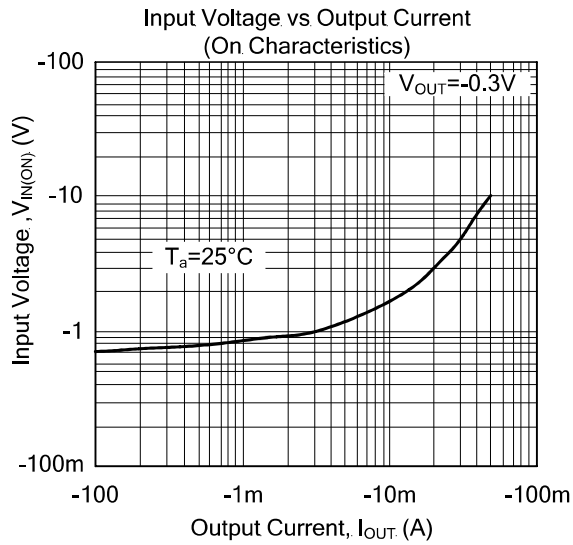
Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
Input voltage	$-V_{CC}=5V, -I_{OUT}=100\mu A$	$-V_{IN(OFF)}$			0.3	V
	$-V_{OUT}=0.3V, -I_{OUT}=1mA$	$-V_{IN(ON)}$	1.4			V
Input current	$-V_{IN}=5V$	$-I_{IN}$			0.88	mA
Output current	$-V_{CC}=50V, V_{IN}=0V$	$-I_{OUT(off)}$			0.5	μA
Output voltage	$-I_{OUT}/-I_{IN}=5mA / 0.25mA$	$-V_{OUT(on)}$		0.1	0.3	V
DC current gain	$-V_{OUT}=5V, -I_{OUT}=5mA$	h_{FE}	68			
Transition frequency	$-V_O=10V, -I_E=5mA, f=100MHz$ (Note)	f_T		250		MHZ
Input resistance		R_1	7	10	13	K Ω
Resistance ratio		R_2 / R_1	3.7	4.7	5.7	

Note : 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

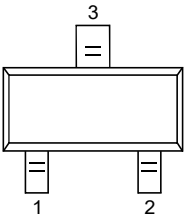
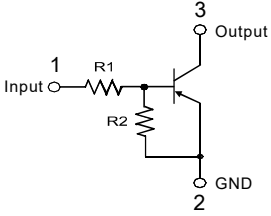
2. Transition frequency of the device.

Rating and characteristic curves (DTA114YE)

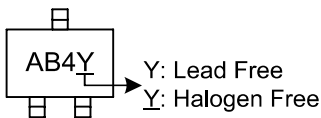


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Pinning information

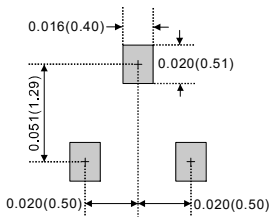
Pin	Simplified outline	Symbol
Pin1 Base Pin2 Emitter Pin3 Collector		

Marking and resistor values



Suggested solder pad layout

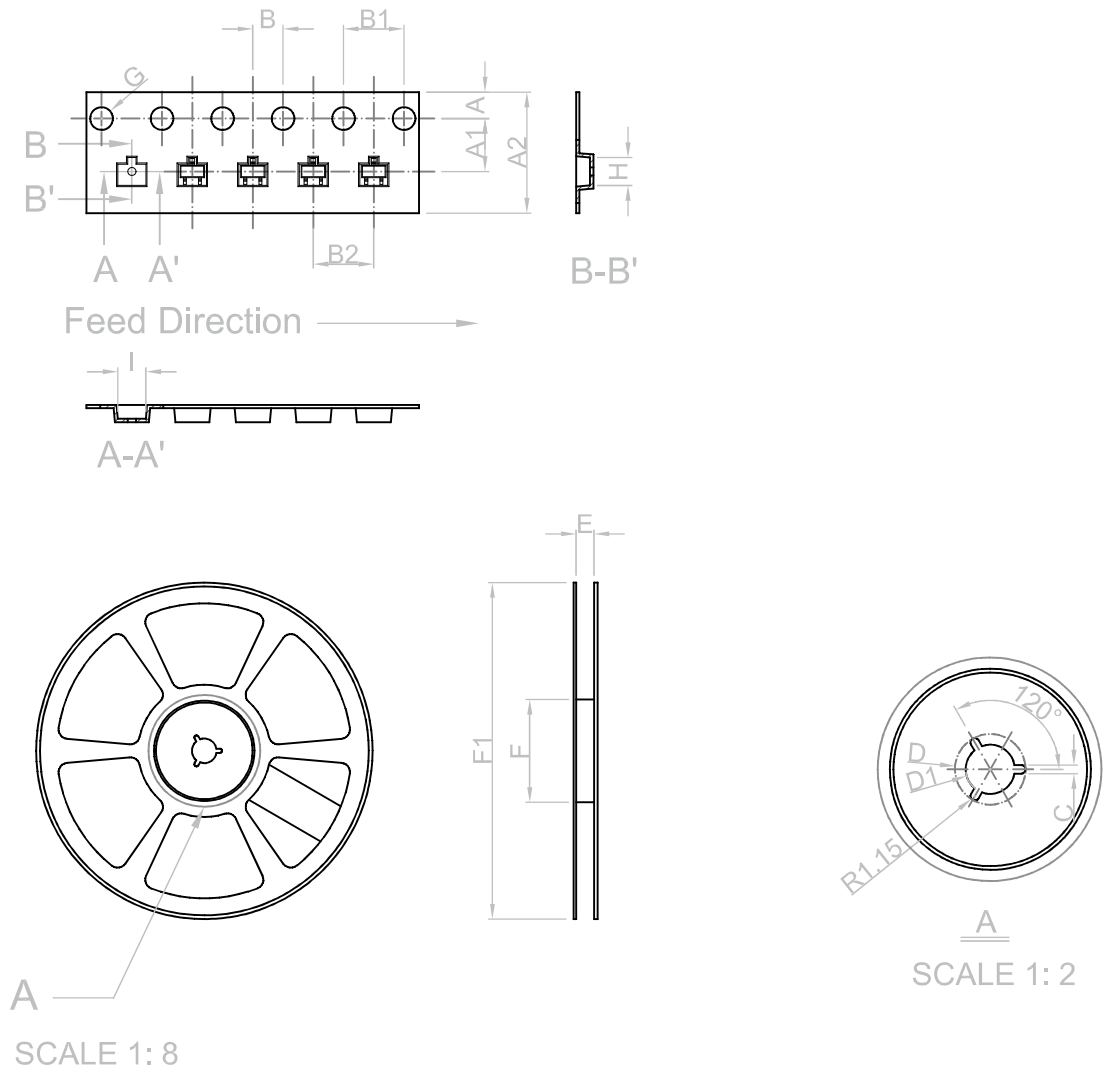
SOT-523



Dimensions in inches and (millimeters)

DTA114YE

Packing information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.65	1.85	0.065	0.073
A1	3.45	3.55	0.136	0.140
A2	7.90	8.20	0.311	0.323
B	1.95	2.05	0.077	0.081
B1	3.90	4.10	0.154	0.161
B2	3.90	4.10	0.154	0.161
C	1.50	3.00	0.059	0.118
D	21.00	22.00	0.827	0.866
D1	12.50	13.50	0.492	0.531
E	8.50	9.50	0.335	0.374
F	53.90	54.90	2.122	2.161
F1	177.00	179.00	6.969	7.047
G	1.50	1.60	0.059	0.063
H	1.80	1.90	0.071	0.075
I	1.80	1.90	0.071	0.075

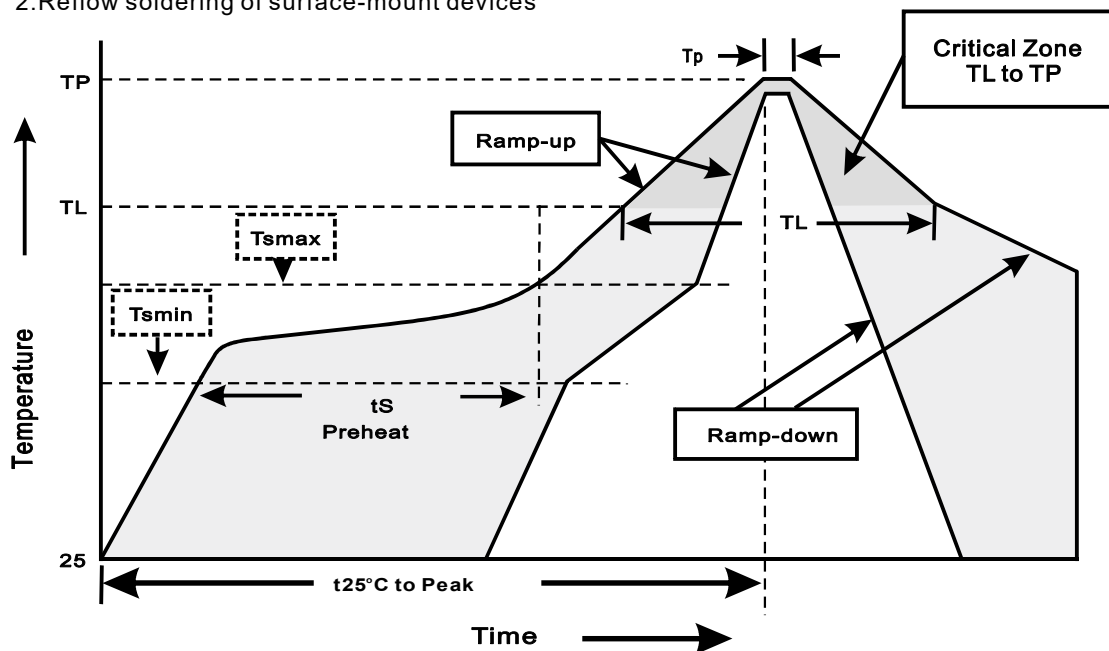
DTA114YE

Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOT-523	7"	3,000	4.0	30,000	183*183*123	178	382*262*387	240,000	9.5

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _p)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes